

AUGER: SURFACE DETECTOR ELECTRONIC UPGRADE

VERSION: 1.12

DATE: 21/03/2016

BILL OF MATERIAL: X002-002-IE00-VA01



**PIERRE
AUGER**
OBSERVATORY

REF: /CAO-PROJETS/SPB16X/LAGORIO/AUGER/SDEU/IE00/

LPSC
53, AVENUE DES MARTYRS
38026 GRENOBLE CEDEX
+33 (0)4.76.28.41.65

ETUDE: SDEU

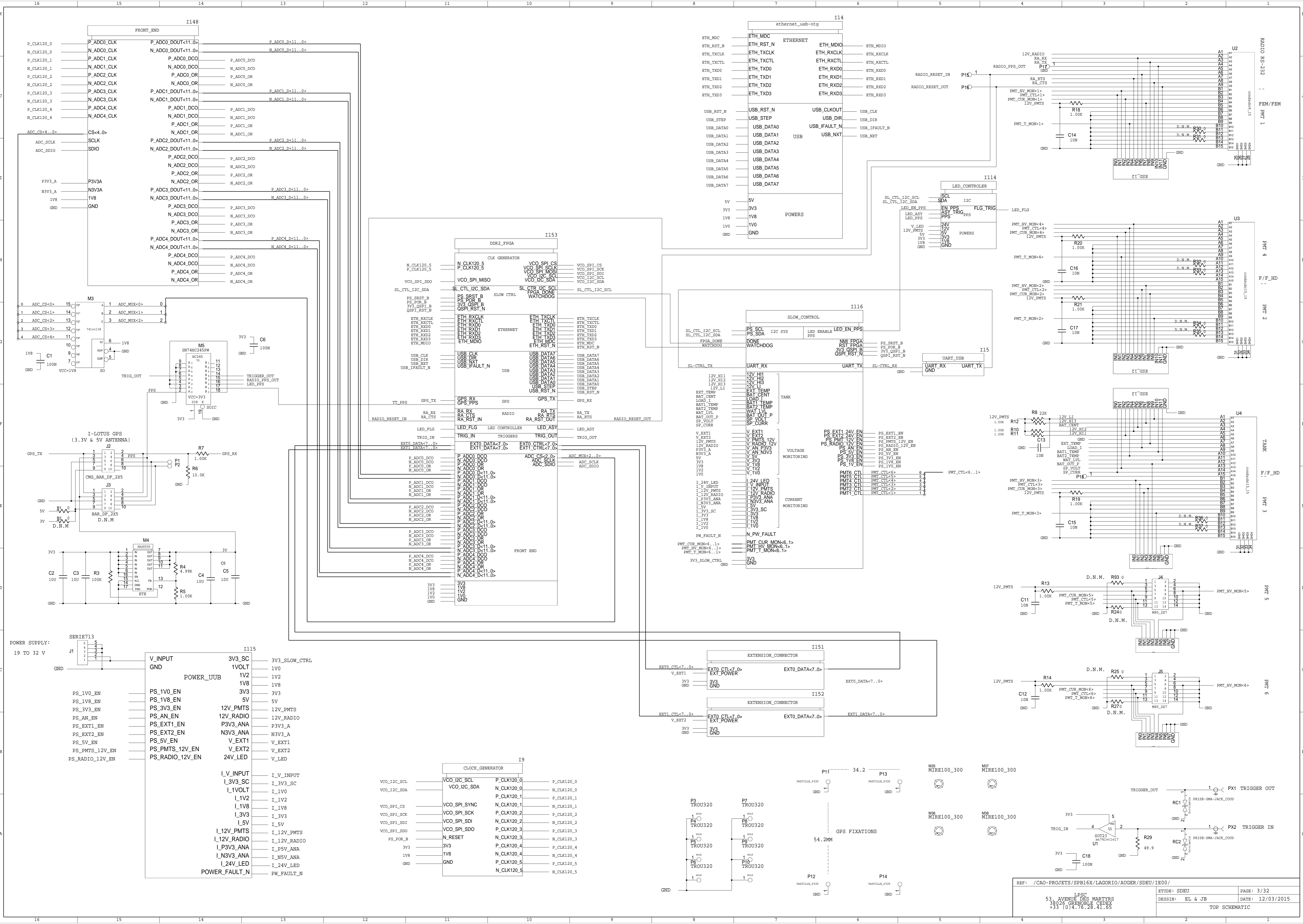
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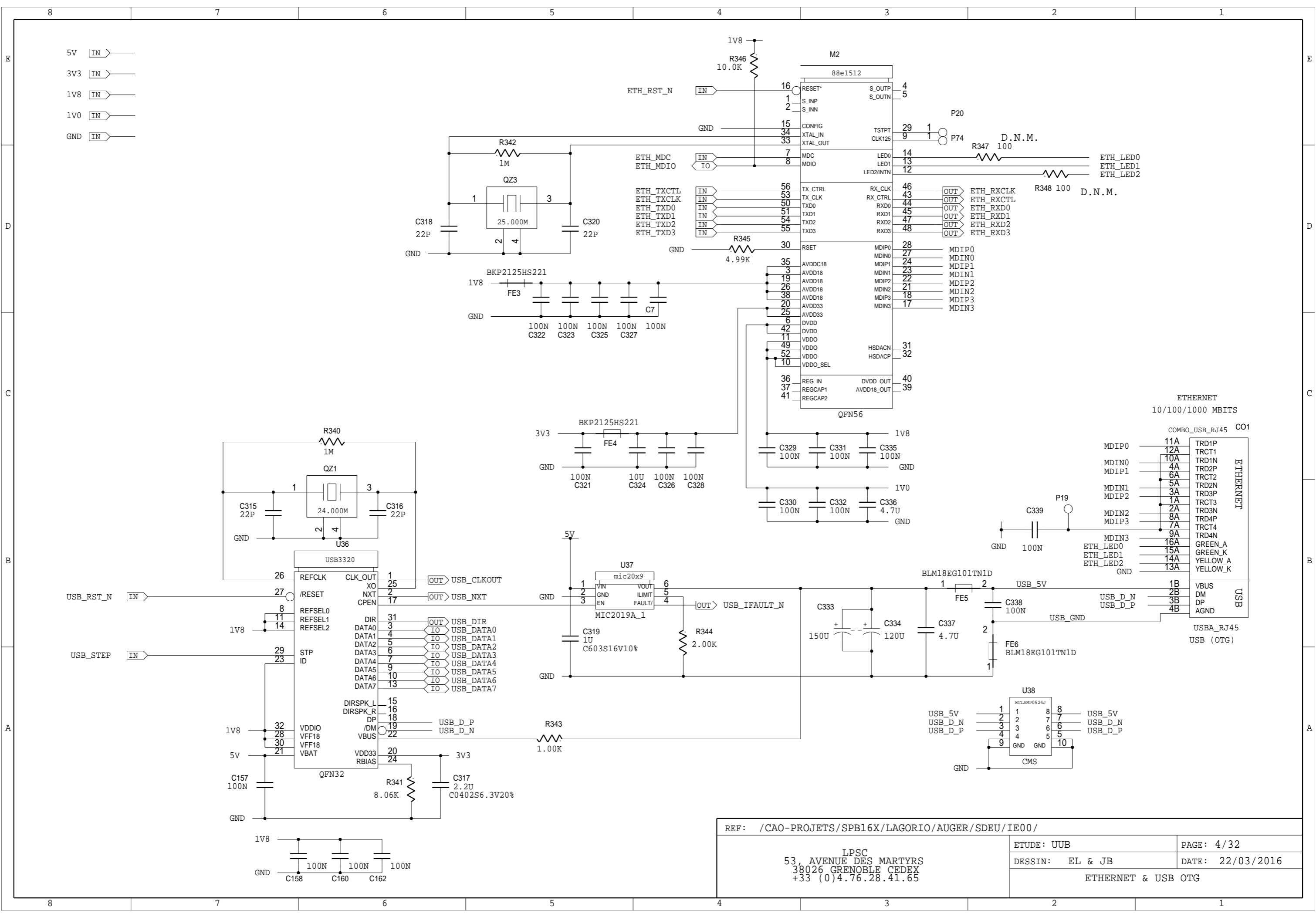
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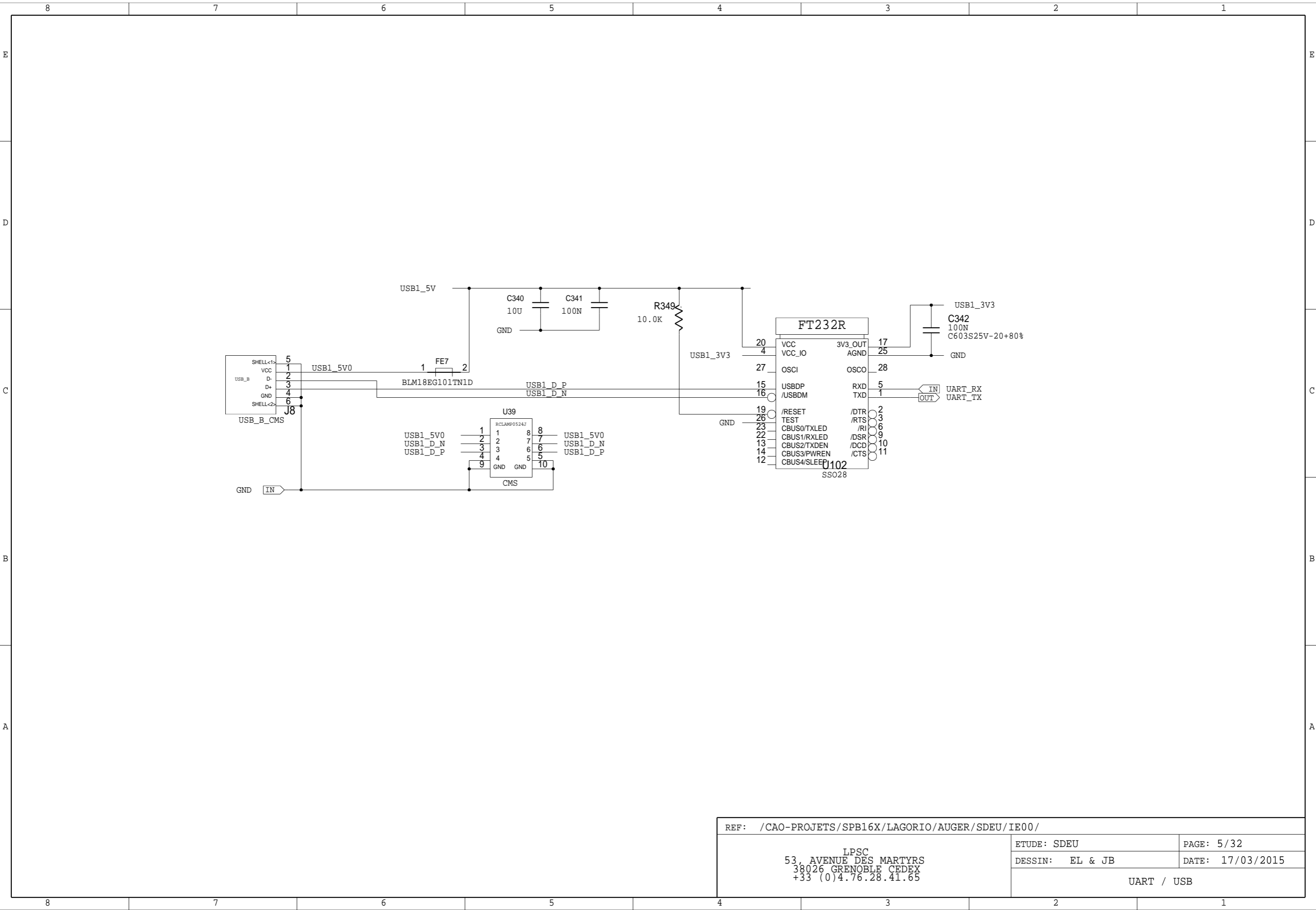
DATE: 22/03/2016

COVER PAGE

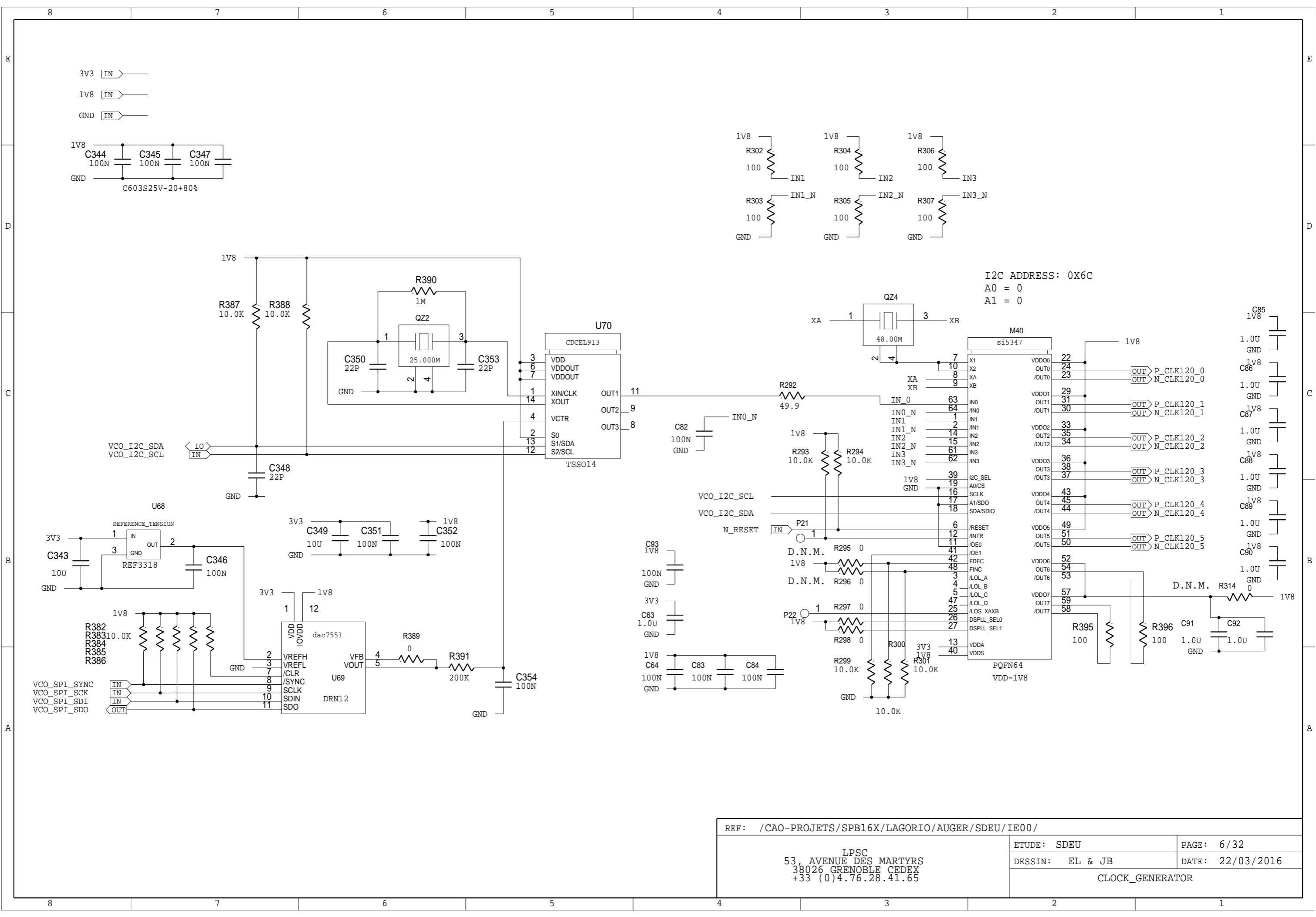
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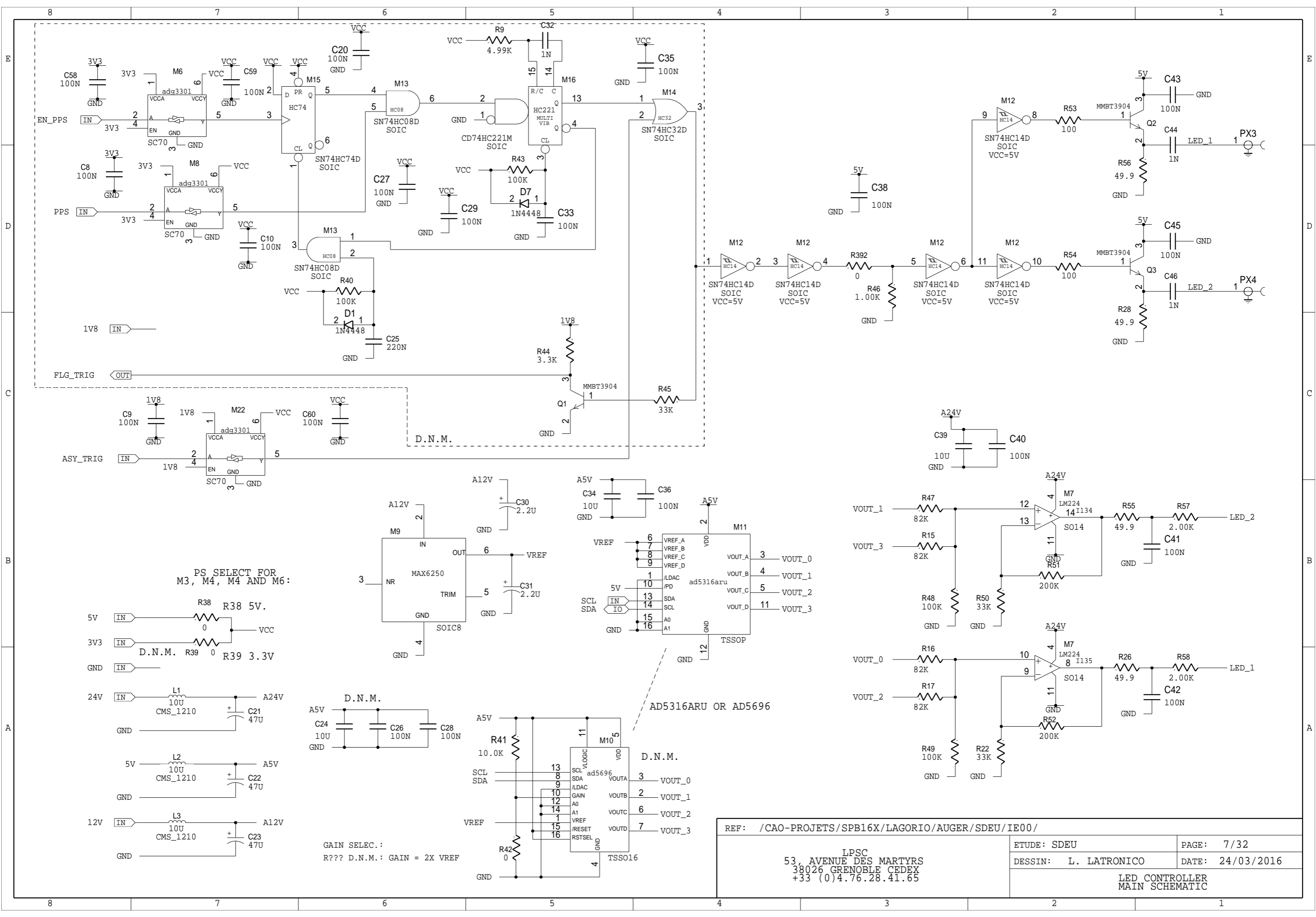


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LPSC 53, AVENUE DES MARTYRS 38026 GRENOBLE CEDEX +33 (0)4.76.28.41.65	ETUDE: SDEU	PAGE: 5/32
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UART / USB		

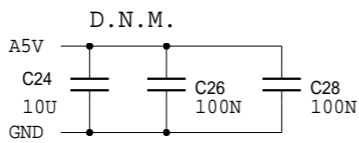
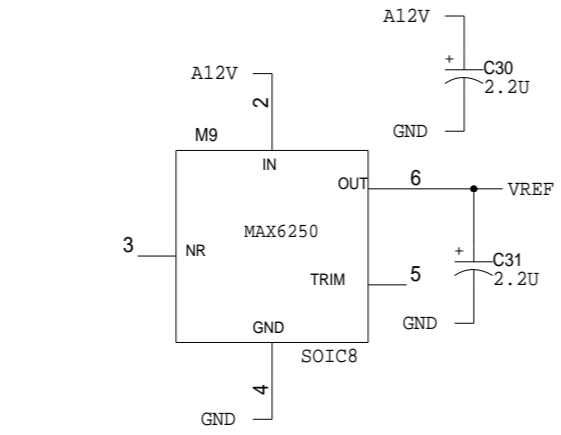
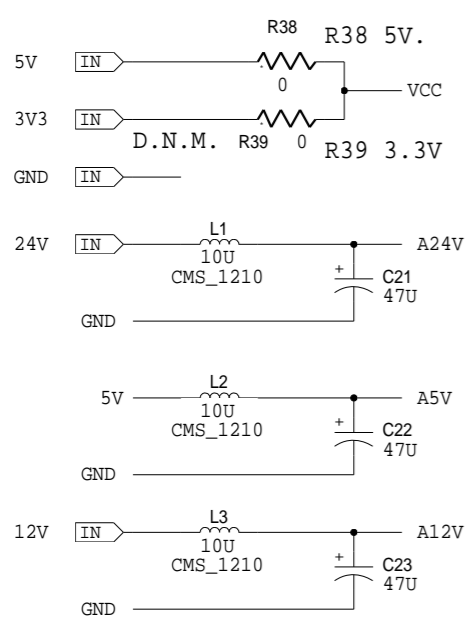


I2C ADDRESS: 0X6C
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 A1 = 0

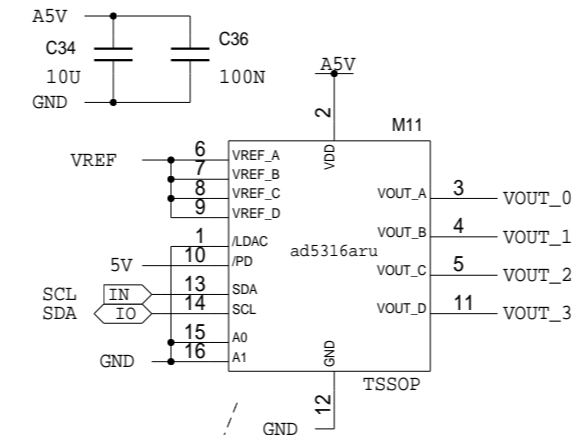
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LPSC 53, AVENUE DES MARTYRS 38026 GRENOBLE CEDEX +33 (0)4.76.28.41.65	ETUDE: SDEU	PAGE: 6/32
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CLOCK_GENERATOR		



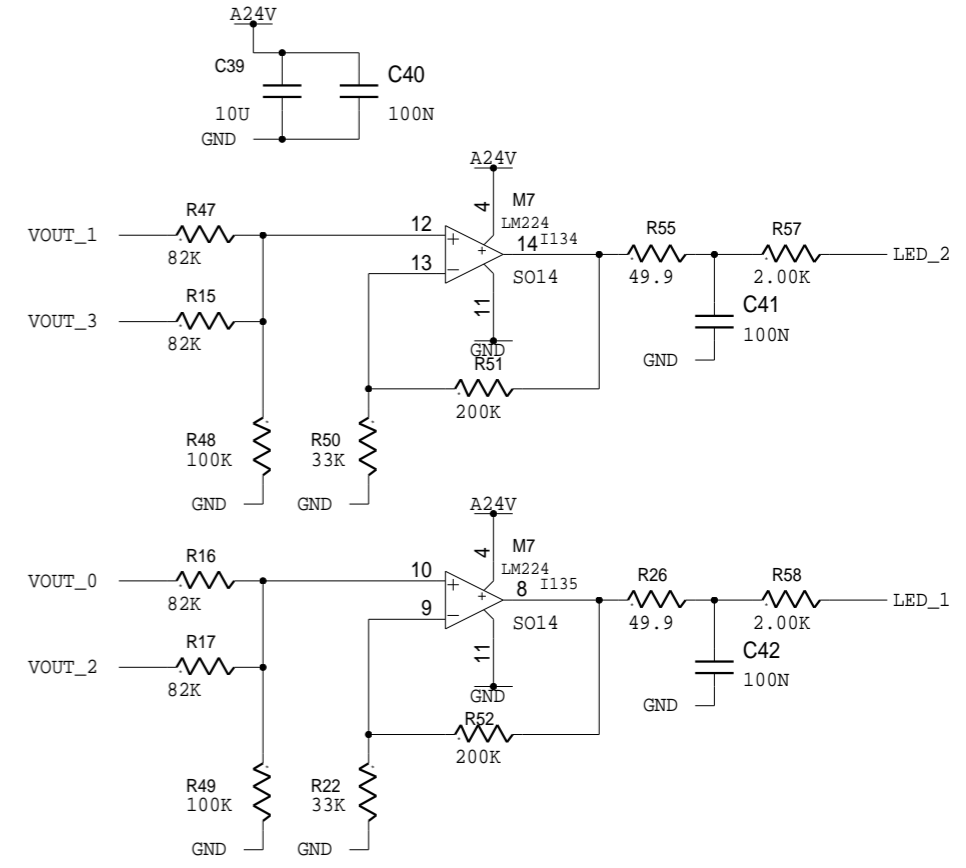
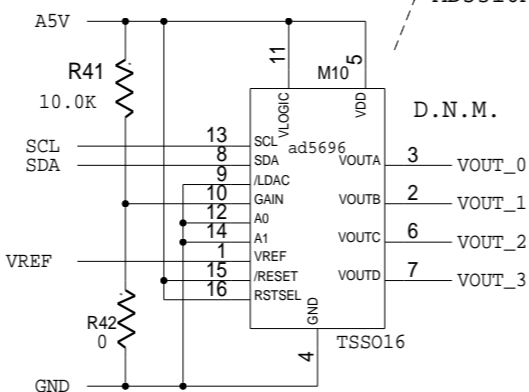
PS SELECT FOR M3, M4, M4 AND M6:



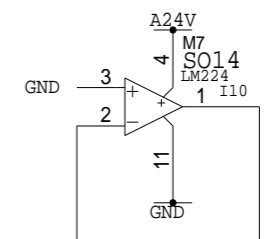
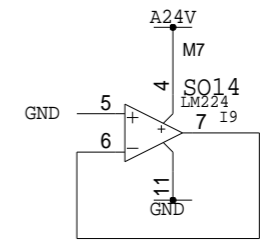
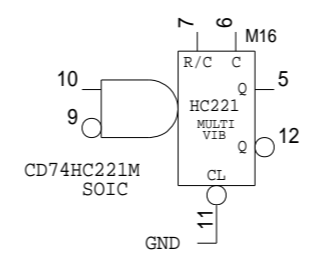
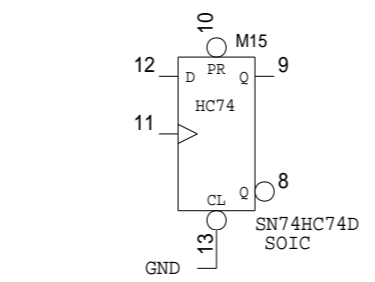
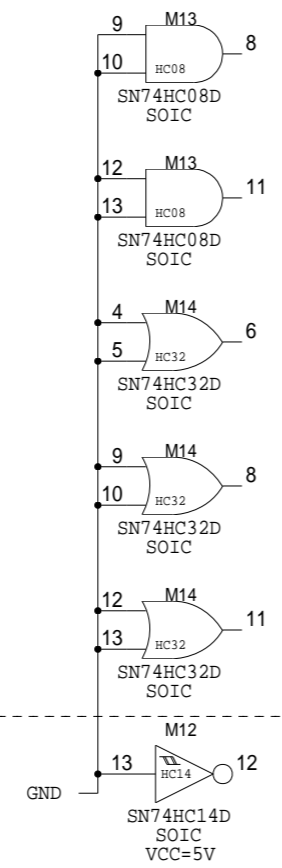
GAIN SELEC.:
 R??? D.N.M.: GAIN = 2X VREF



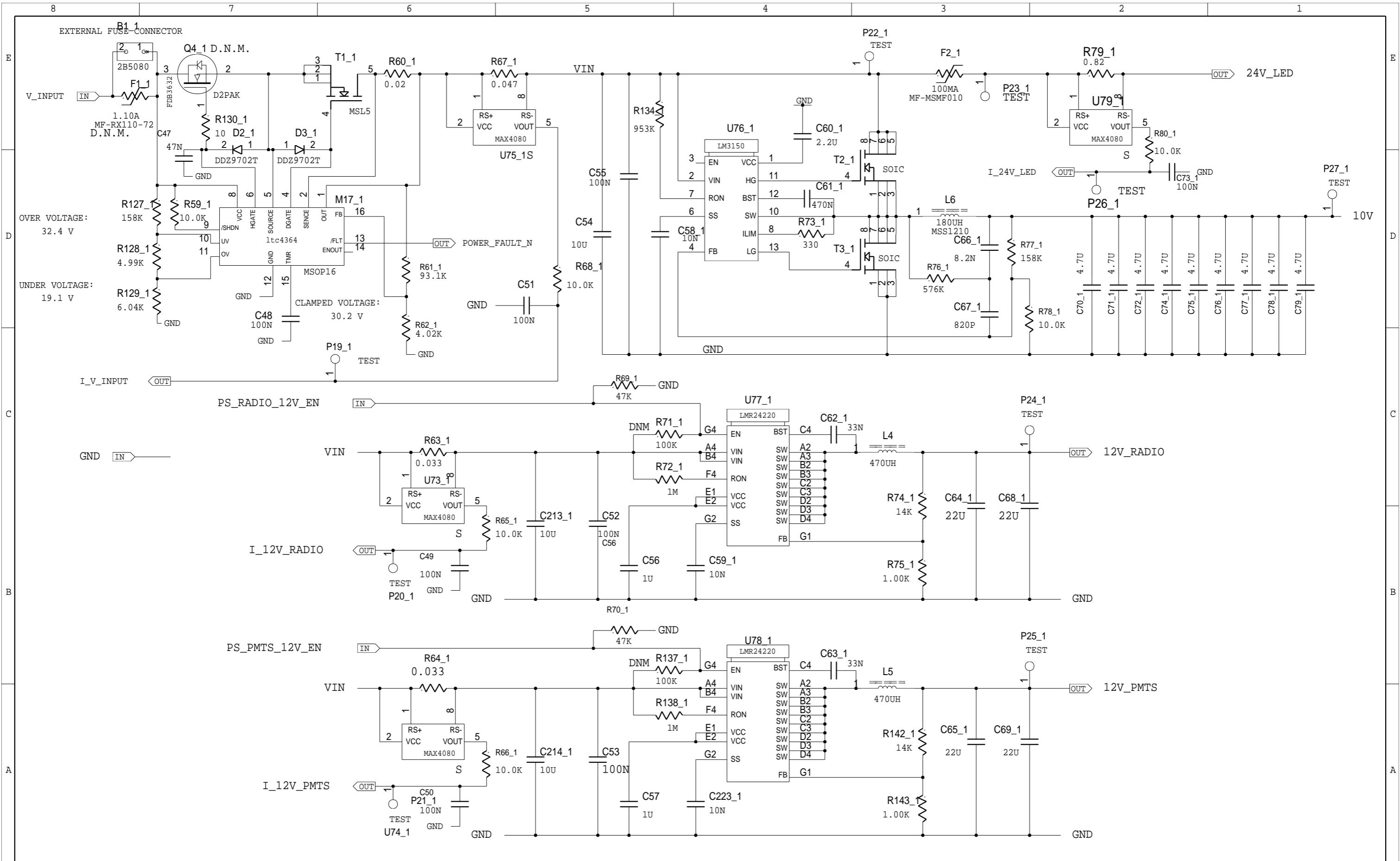
AD5316ARU OR AD5696

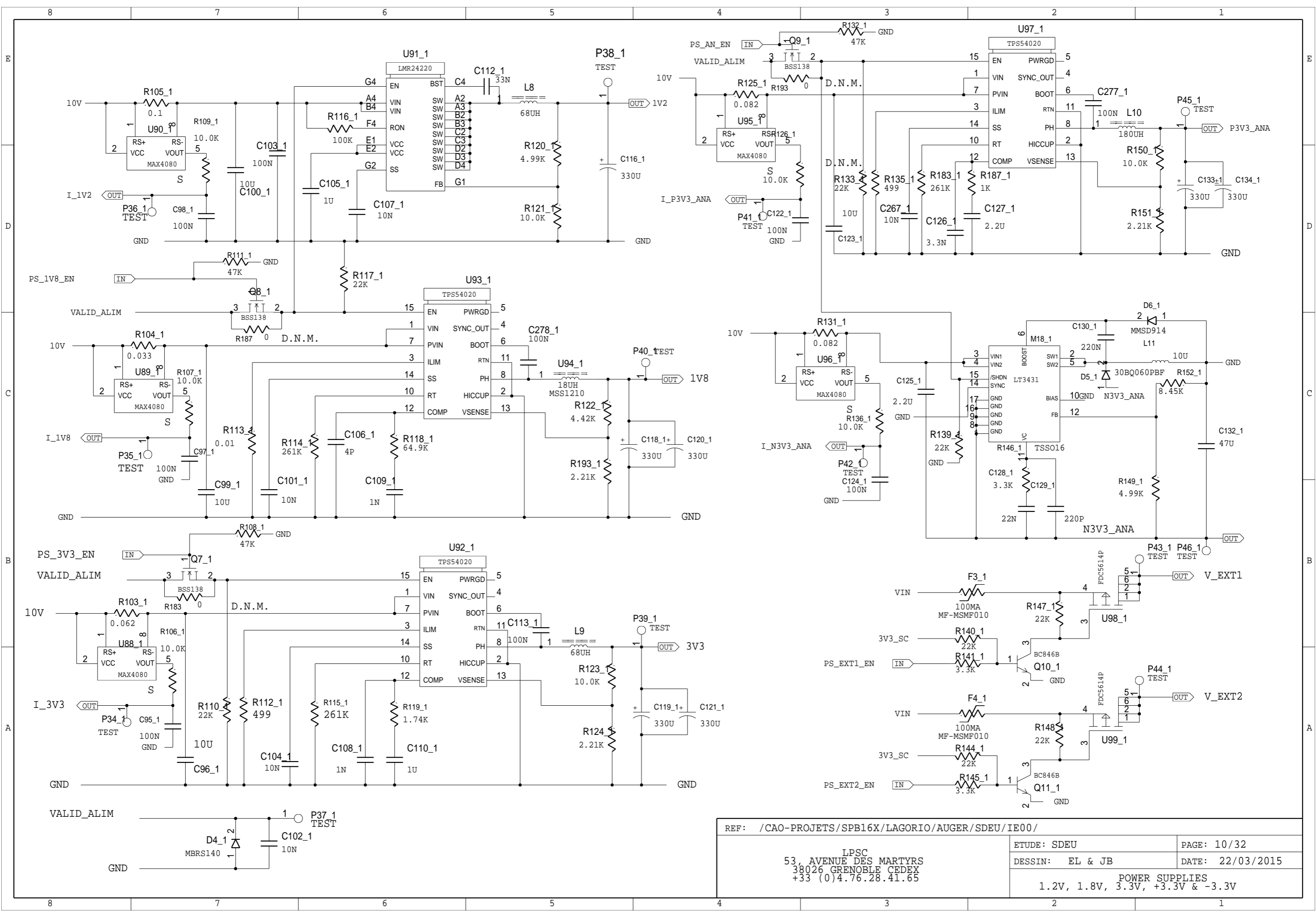


D.N.M.

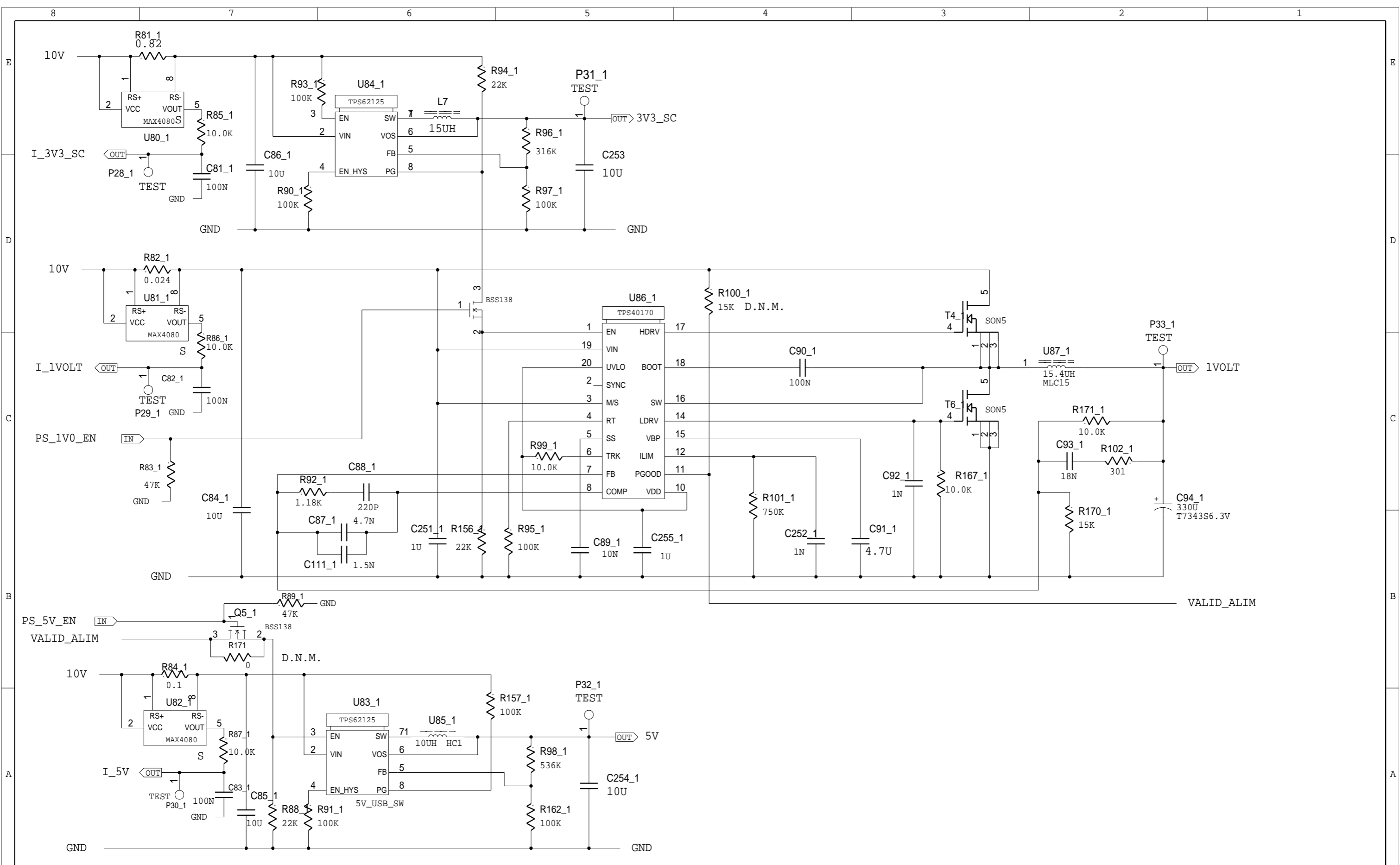


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LPSC 53, AVENUE DES MARTYRS 38026 GRENOBLE CEDEX +33 (0)4.76.28.41.65		DESSIN: L. LATRONICO	DATE: 22/03/2016
		LED CONTROLLER SPARE COMPONENTS	

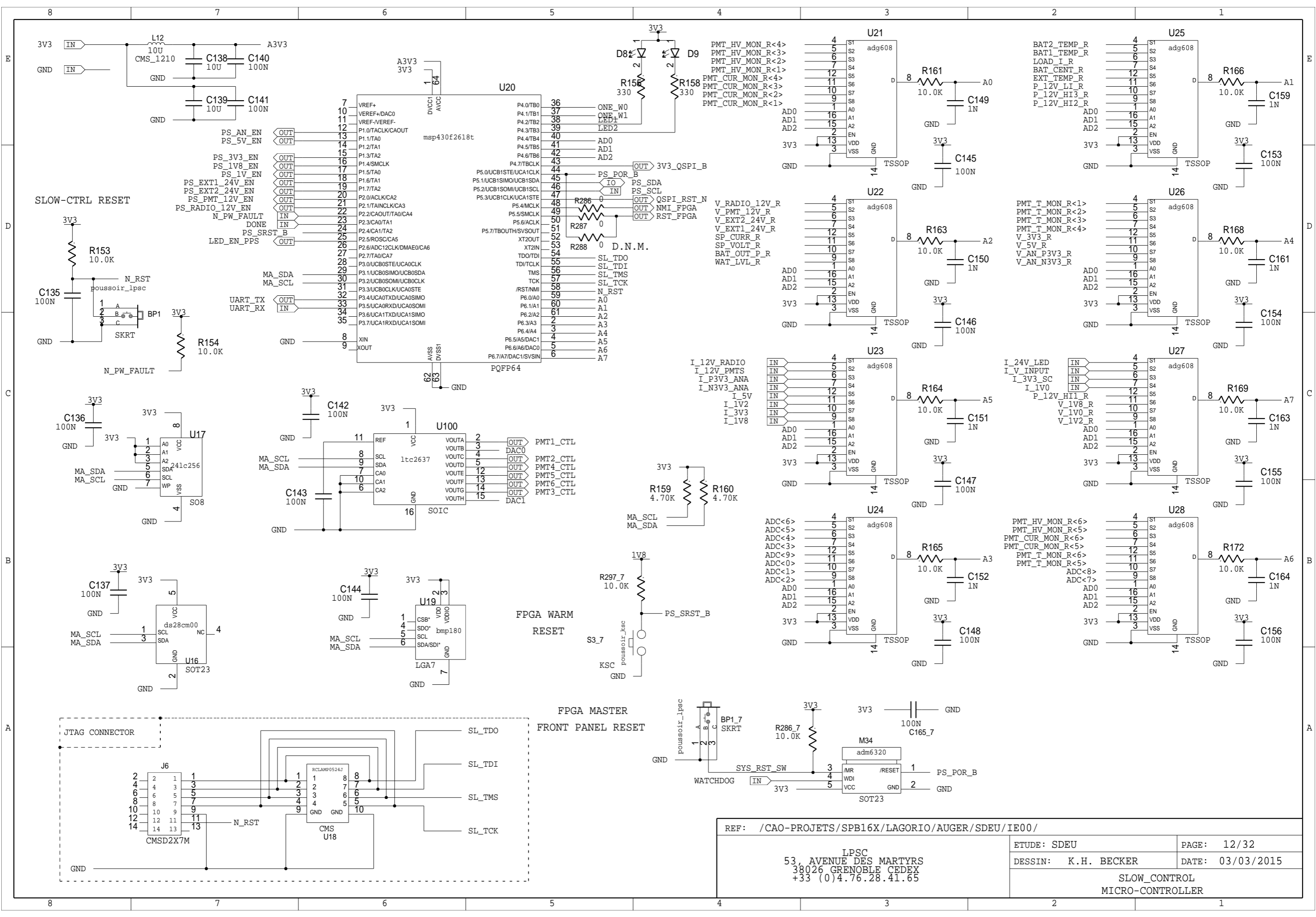


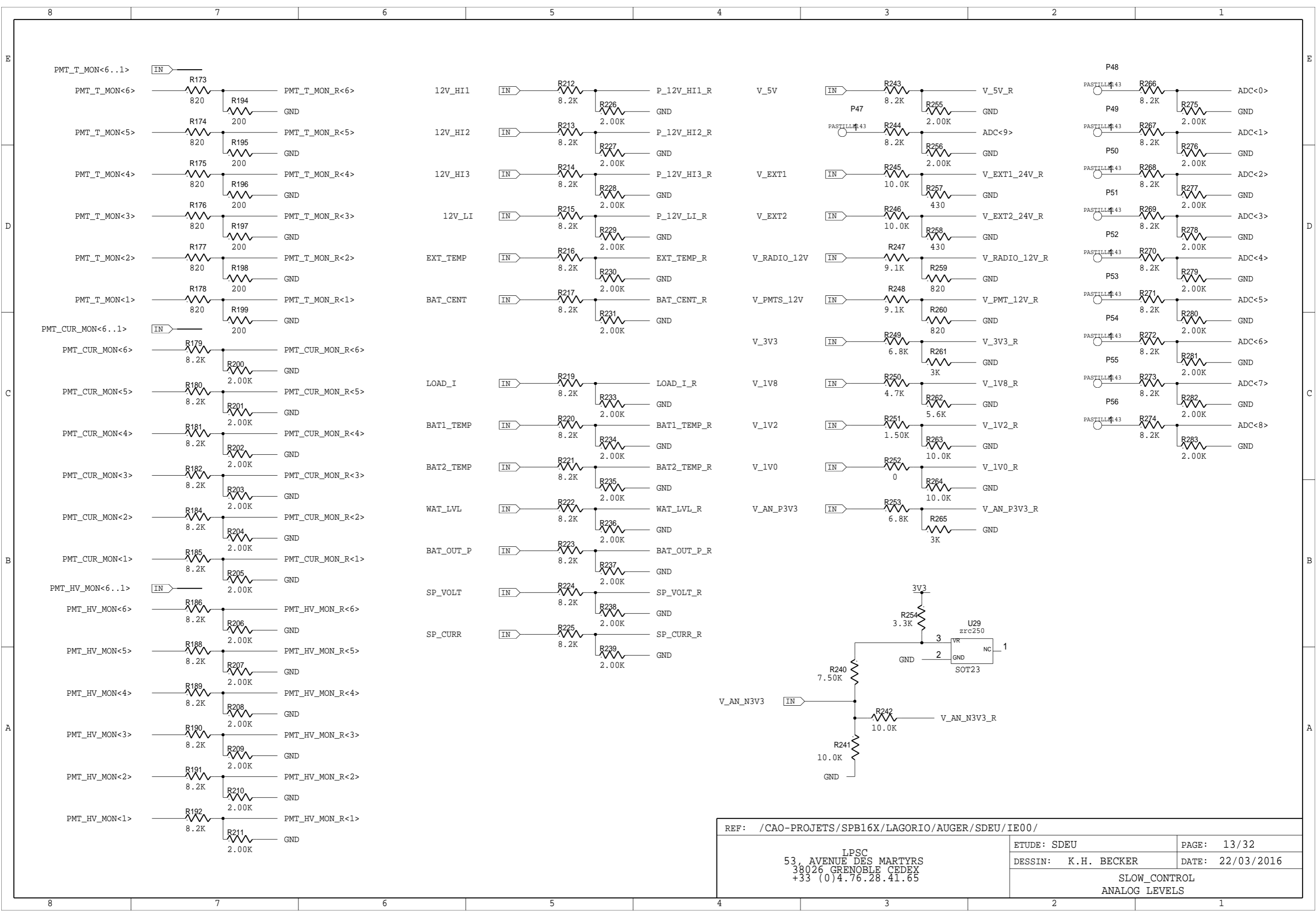


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LPSC 53, AVENUE DES MARTYRS 38026 GRENOBLE CEDEX +33 (0)4.76.28.41.65		
POWER SUPPLIES 1.2V, 1.8V, 3.3V, +3.3V & -3.3V		

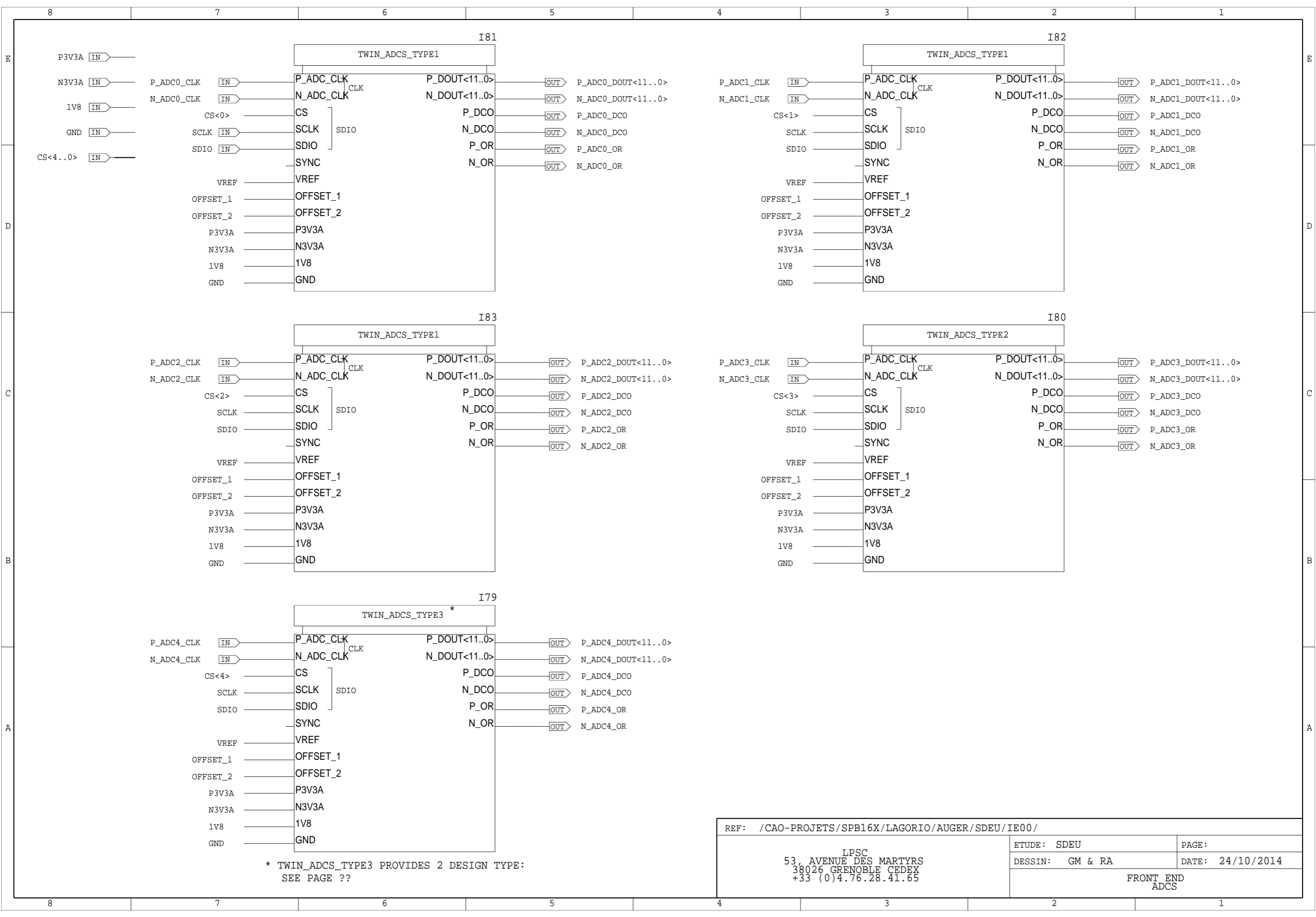


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		PAGE: 11/32 DATE: 22/03/2015
POWER SUPPLIES SLOW-CTRL 3.3V, 1V AND 5V		



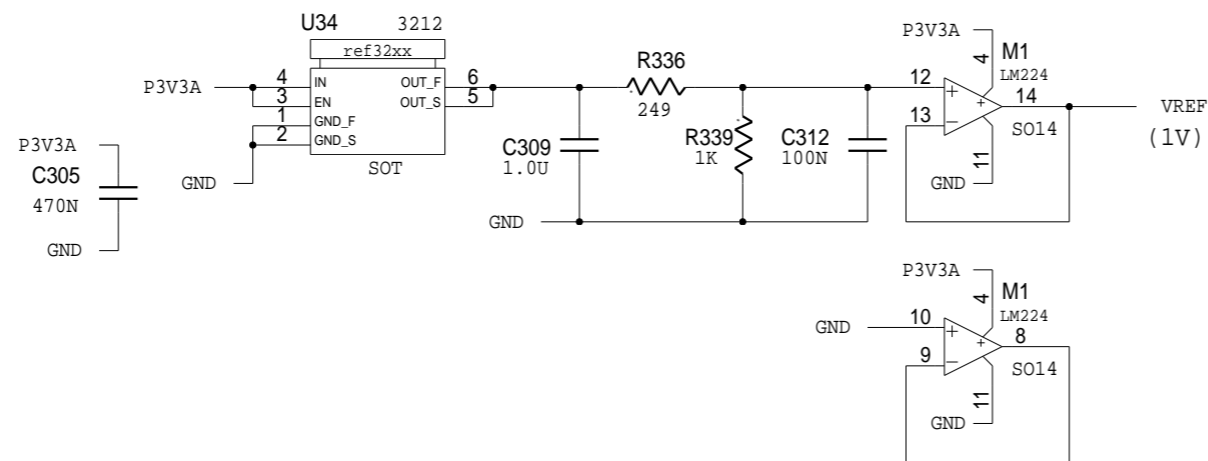
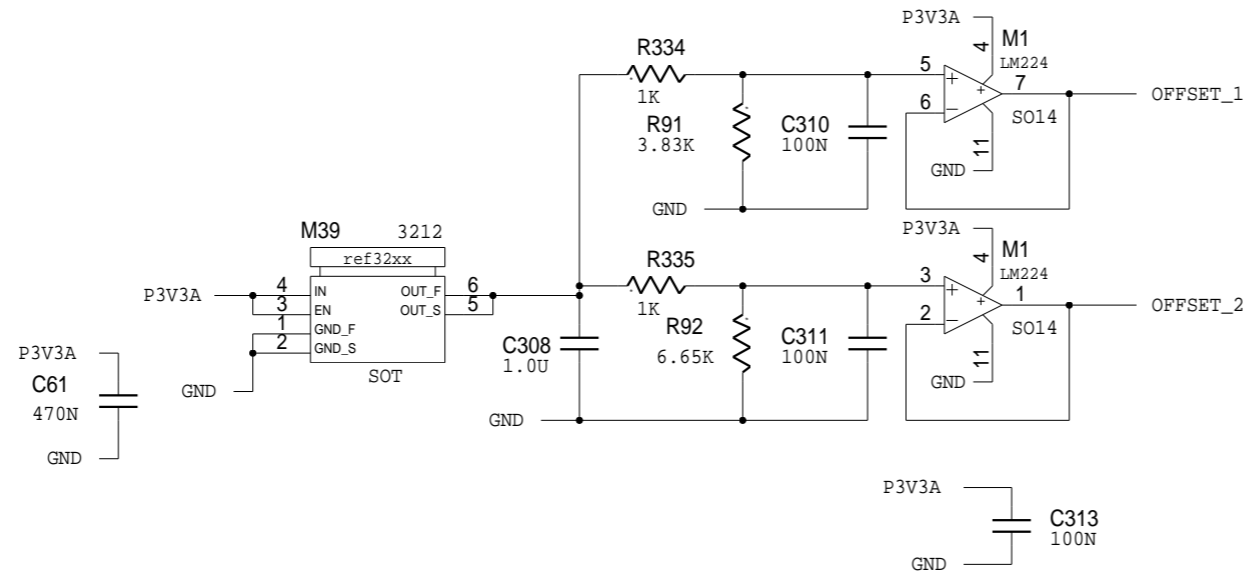


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		PAGE: 13/32 DATE: 22/03/2016
SLOW CONTROL ANALOG LEVELS		



* TWIN_ADCS_TYPE3 PROVIDES 2 DESIGN TYPE:
SEE PAGE ??

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LPSC 53, AVENUE DES MARTYRS 38026 GRENOBLE CEDEX +33 (0)4.76.28.41.65		DESSIN: GM & RA	DATE: 24/10/2014
		FRONT END ADCS	



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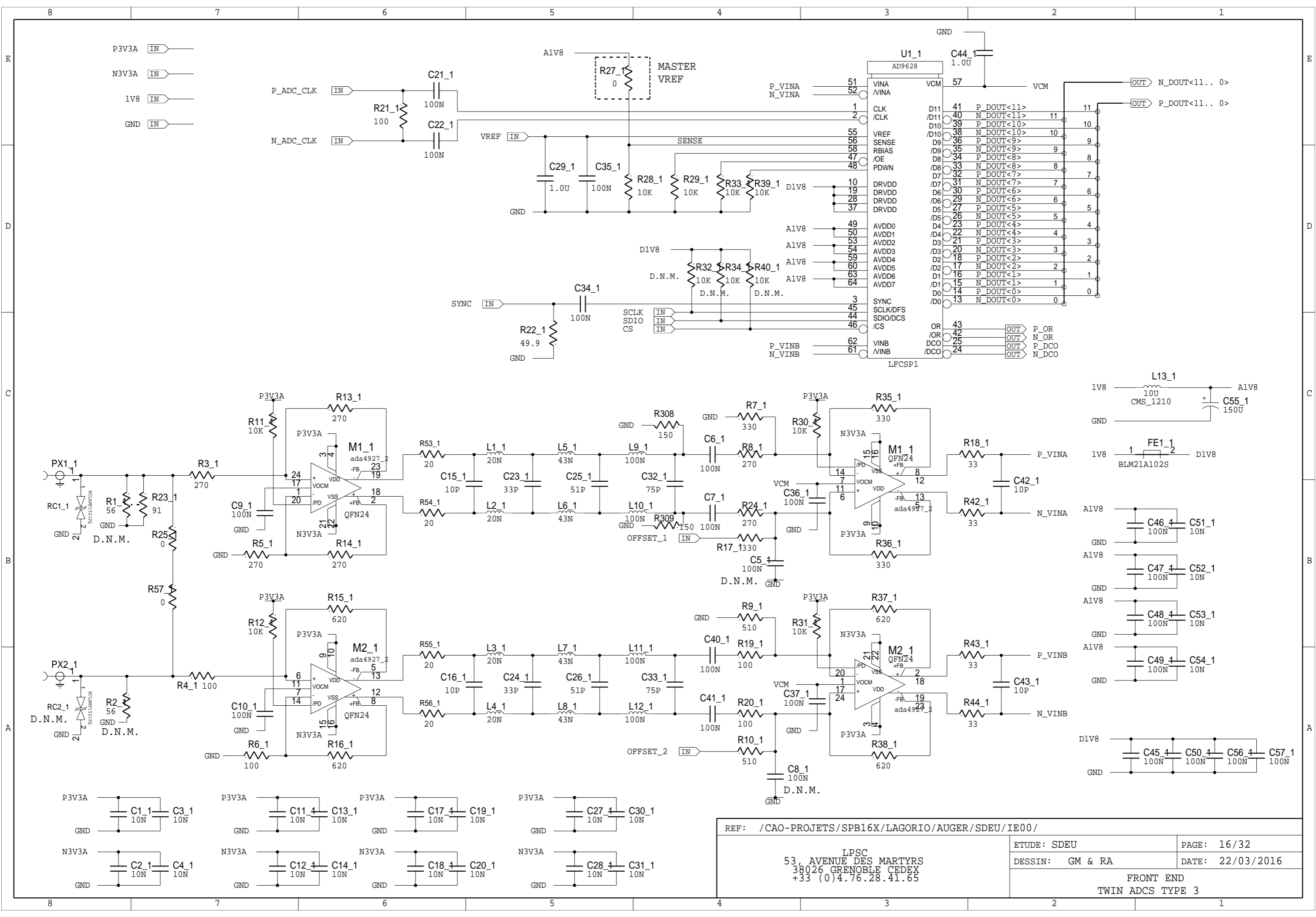
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DESSIN: GM & RA

DATE: 22/03/2016

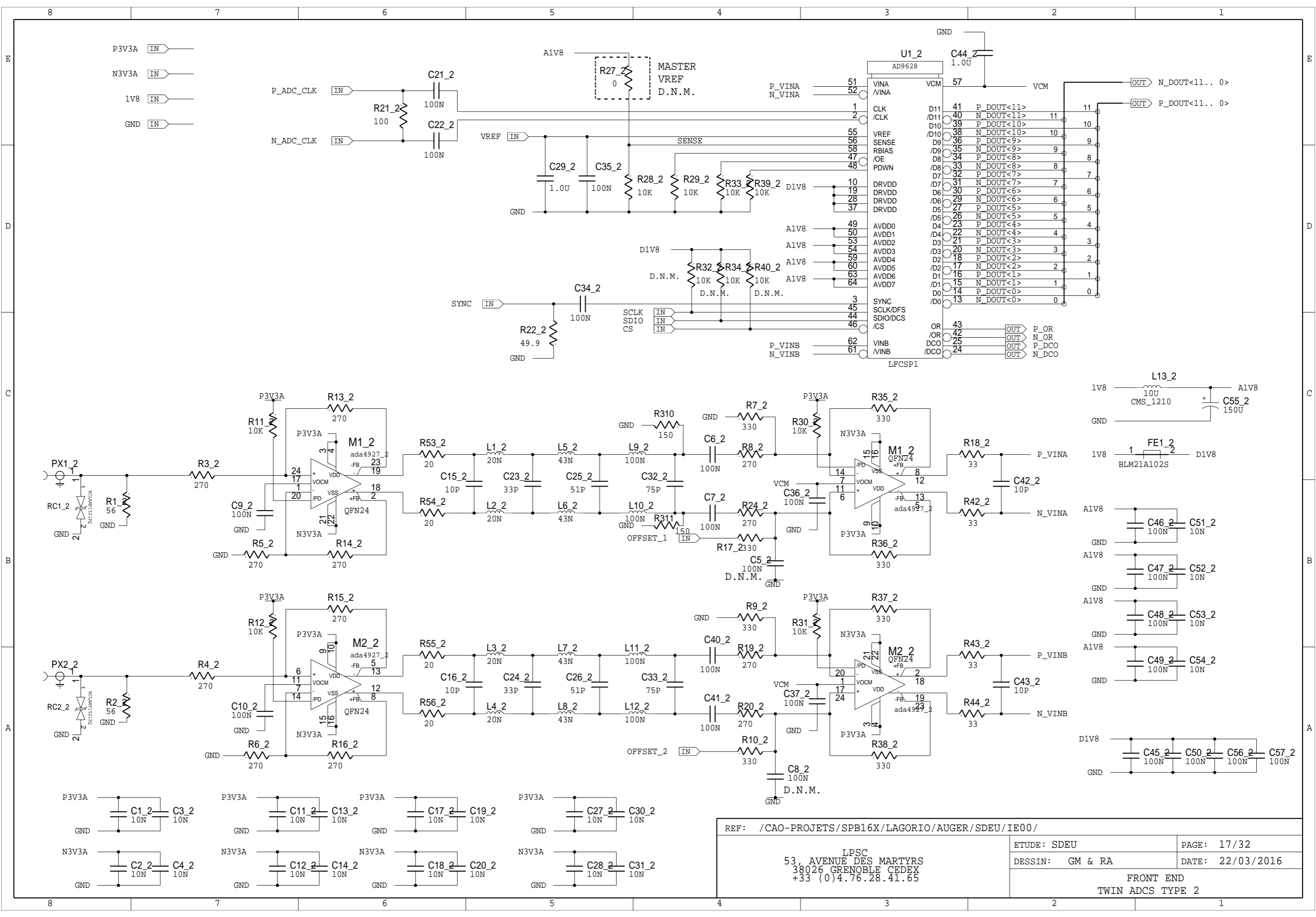
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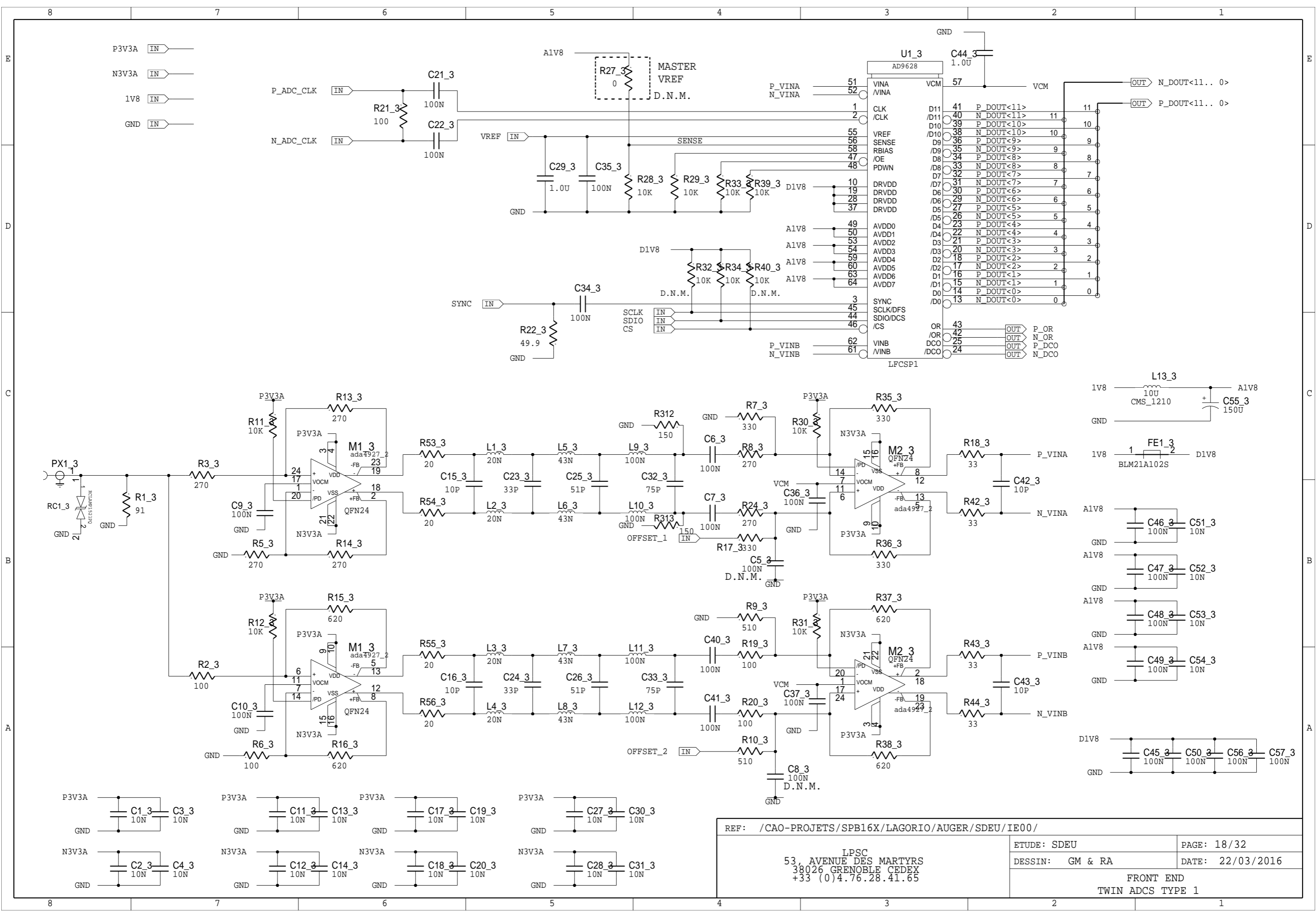
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FRONT END TWIN ADCS TYPE 3	



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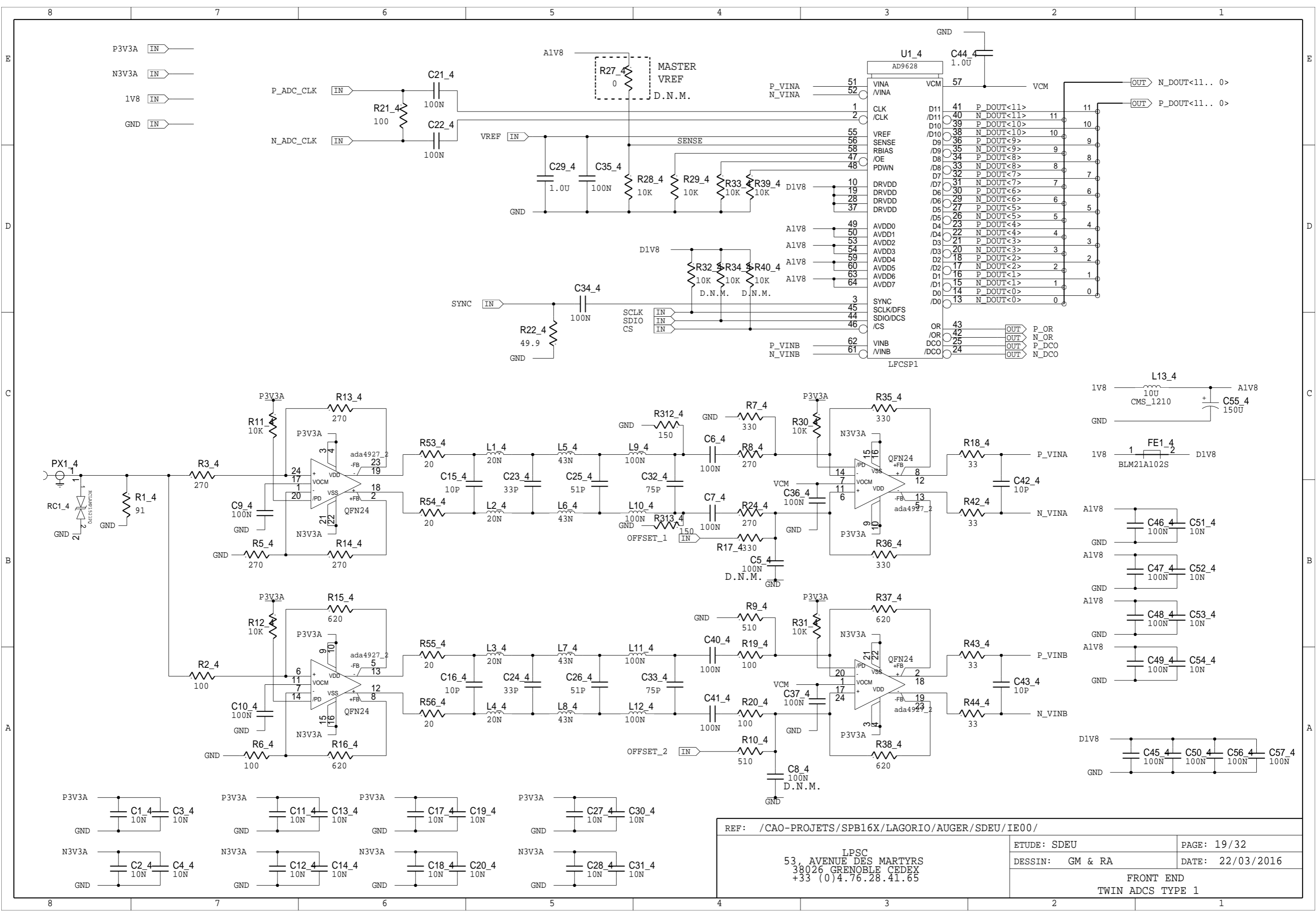
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FRONT END TWIN ADCS TYPE 2	



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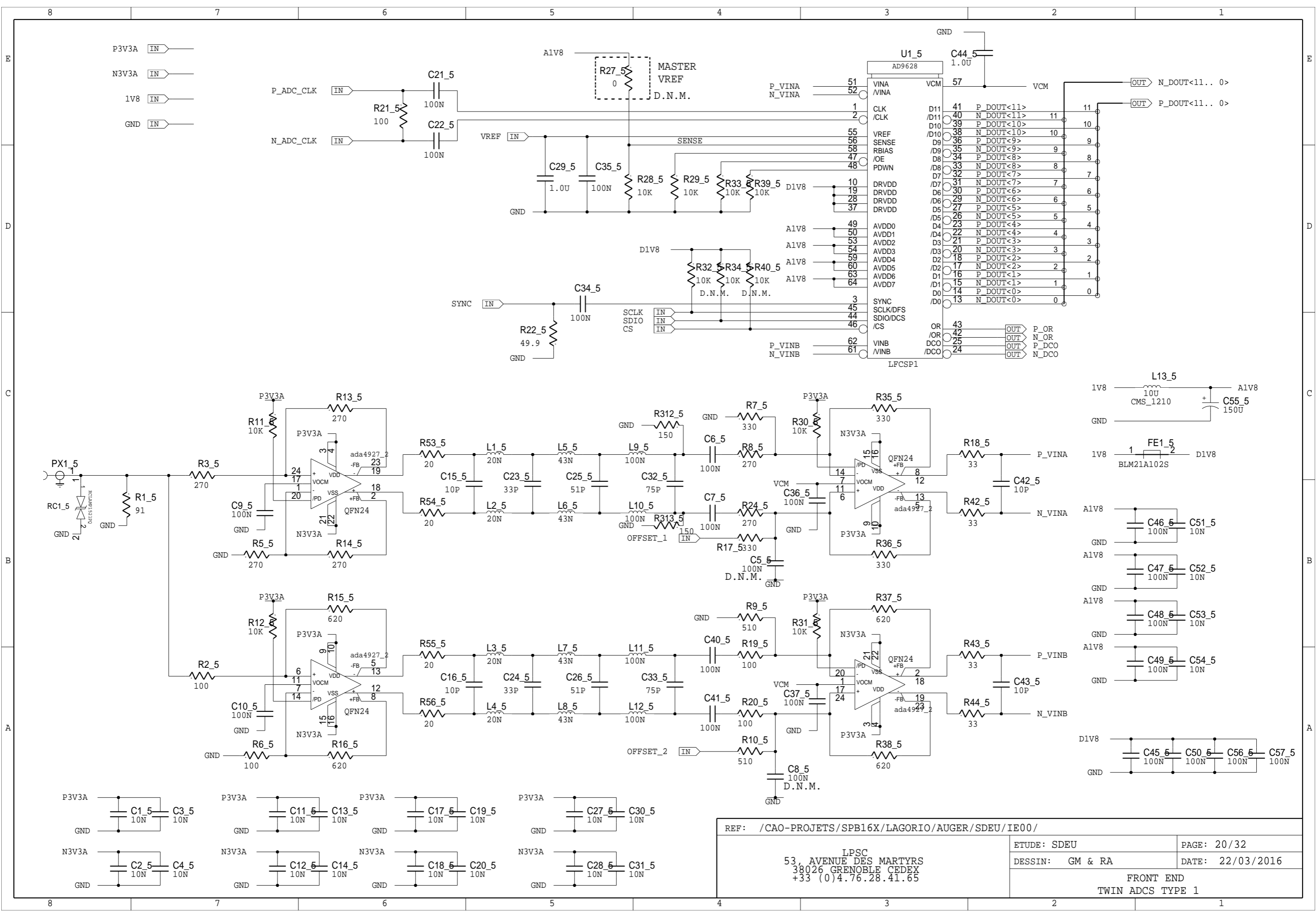
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DESSIN: GM & RA	DATE: 22/03/2016
FRONT END TWIN ADCS TYPE 1	



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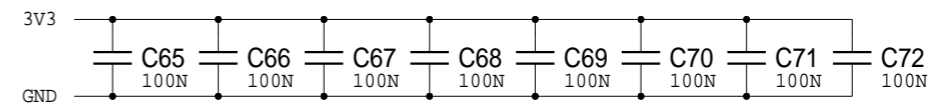
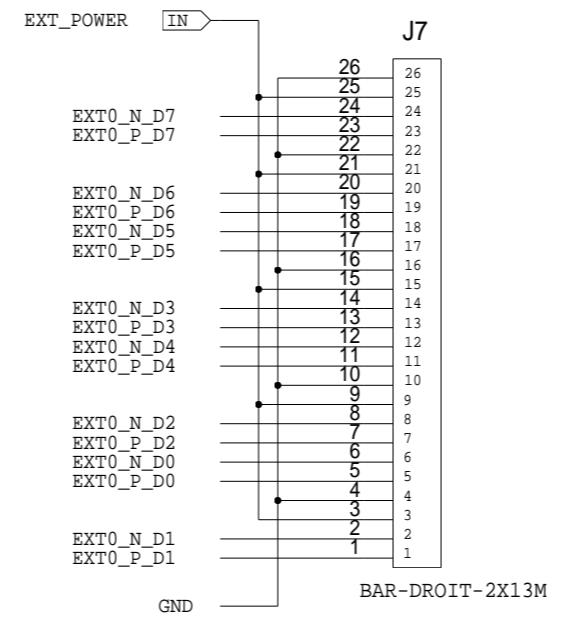
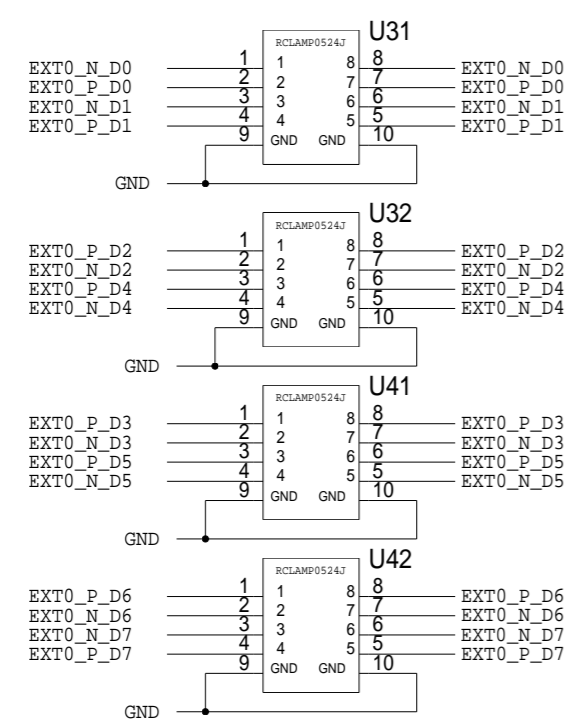
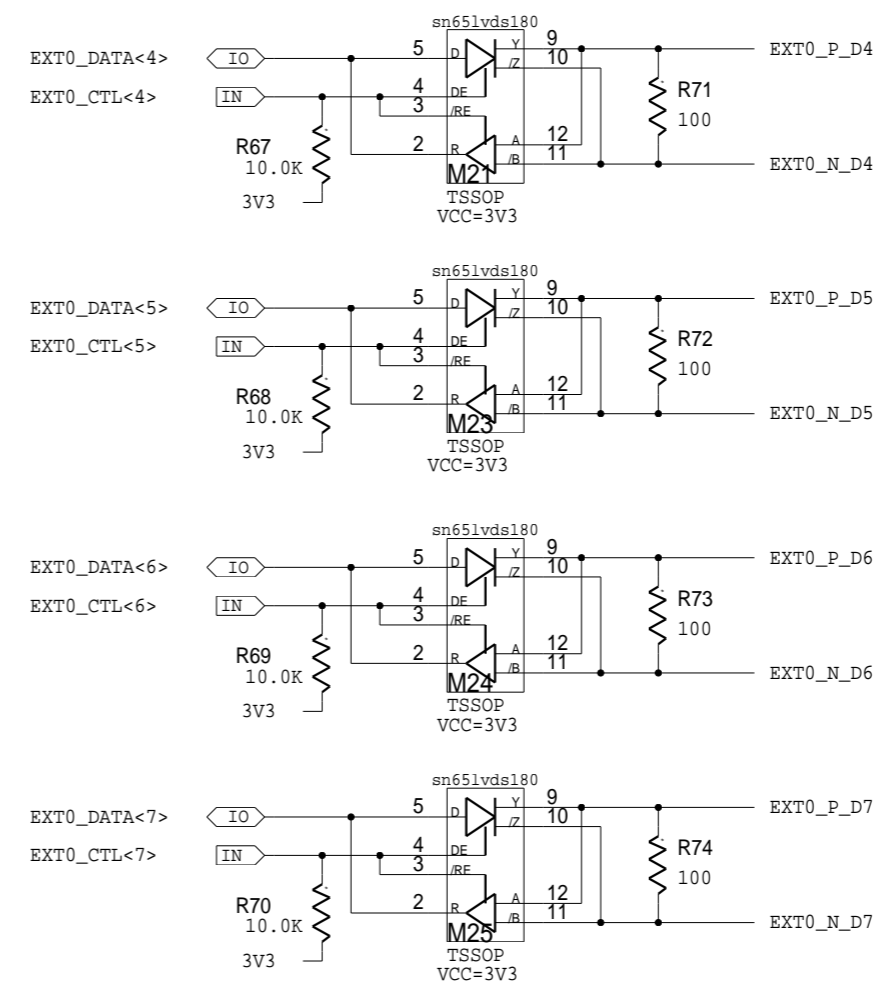
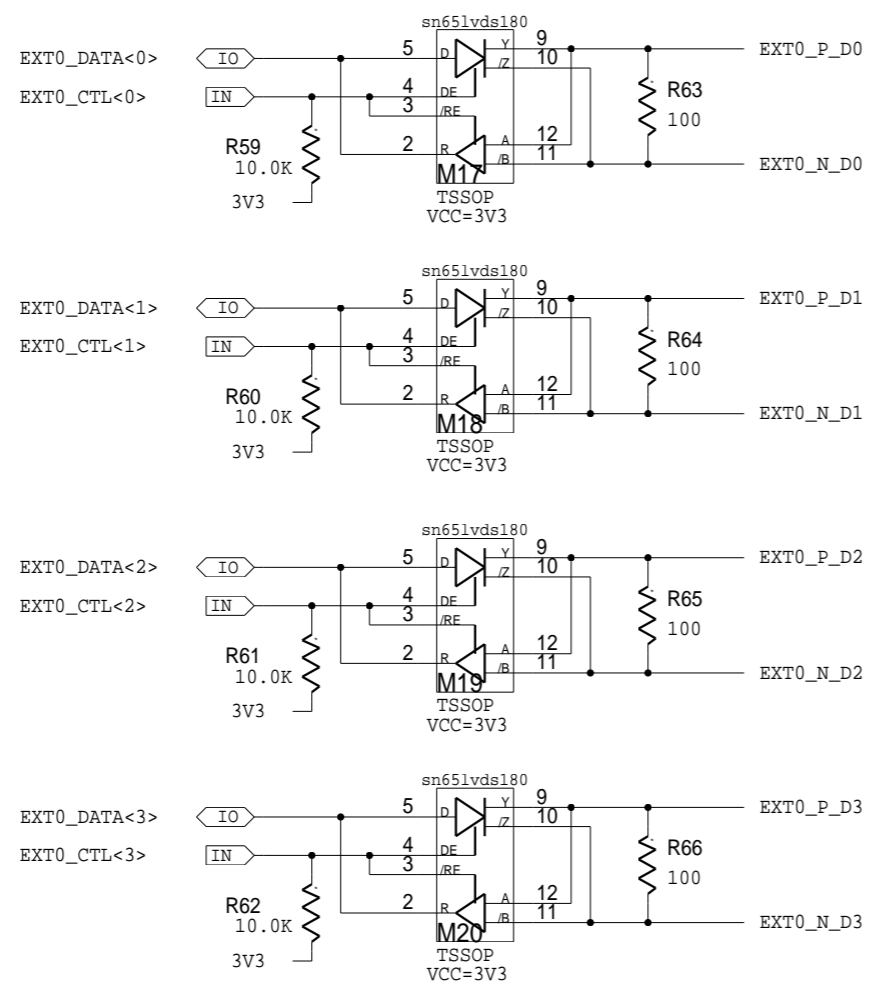
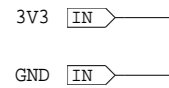
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DESSIN: GM & RA	DATE: 22/03/2016
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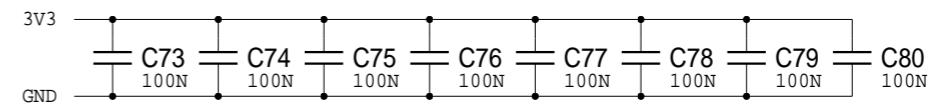
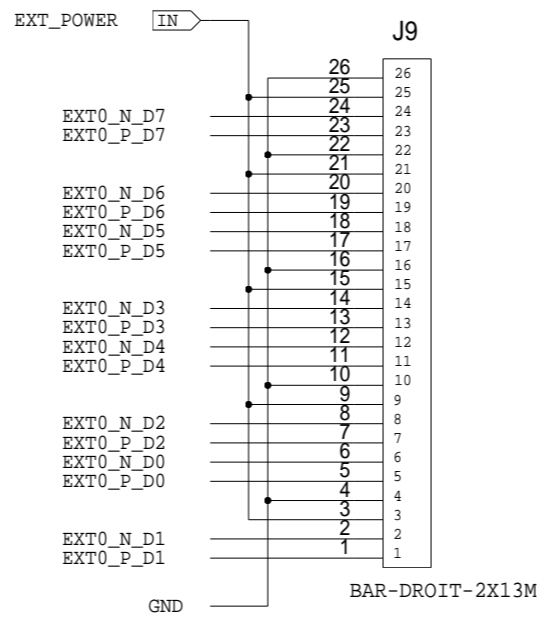
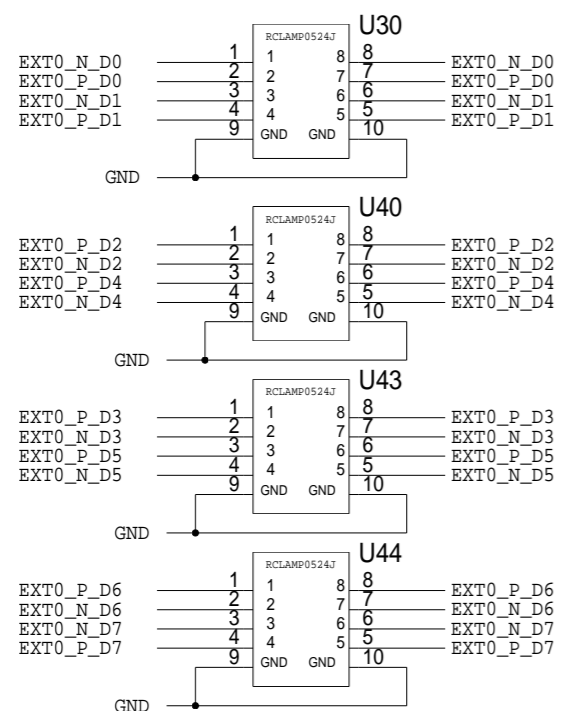
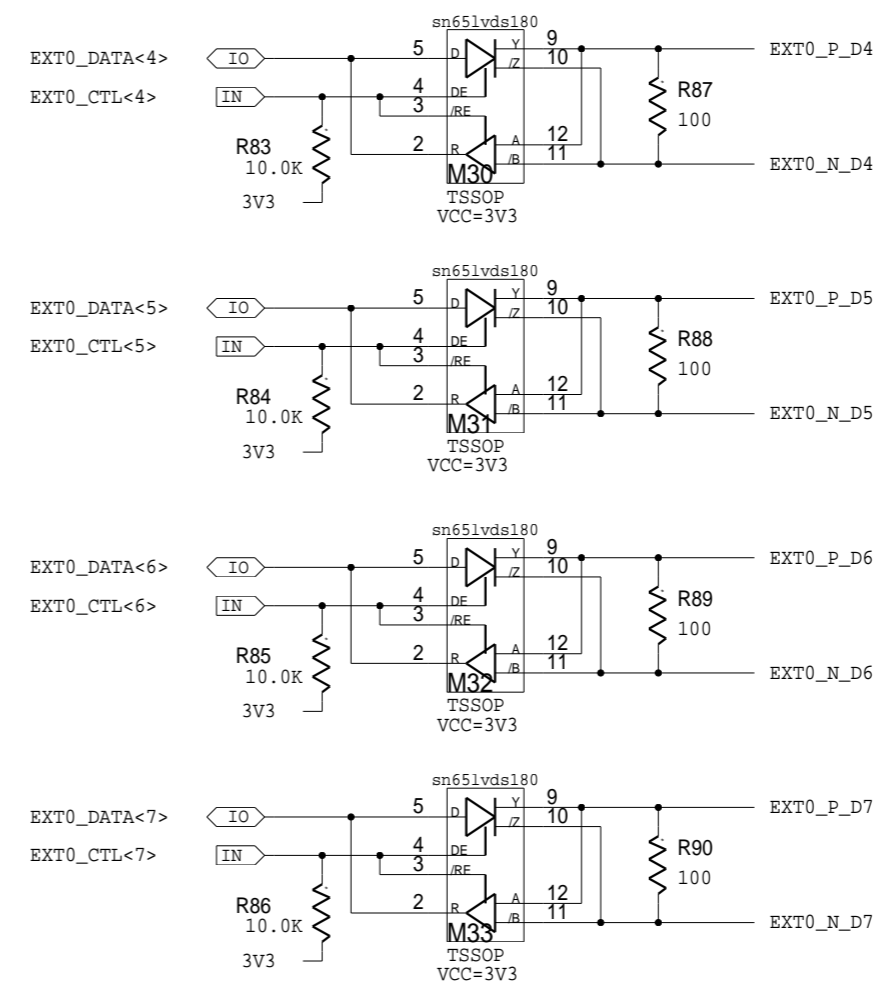
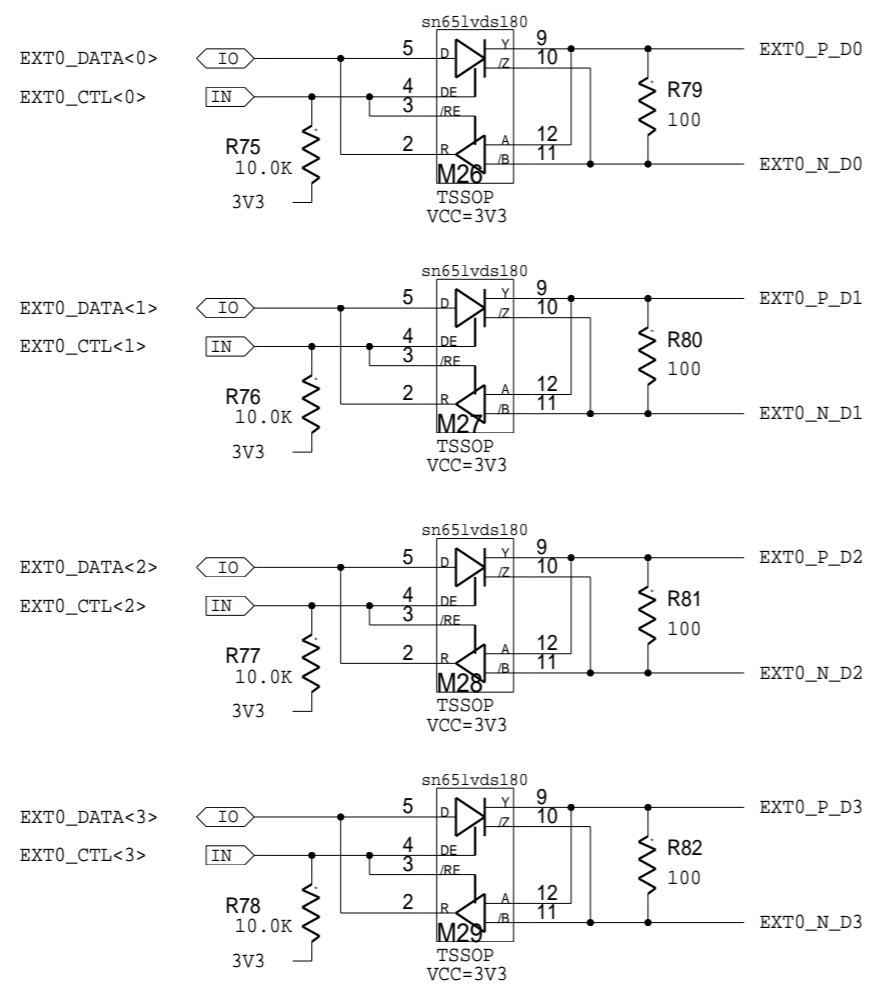
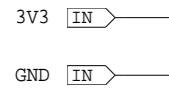
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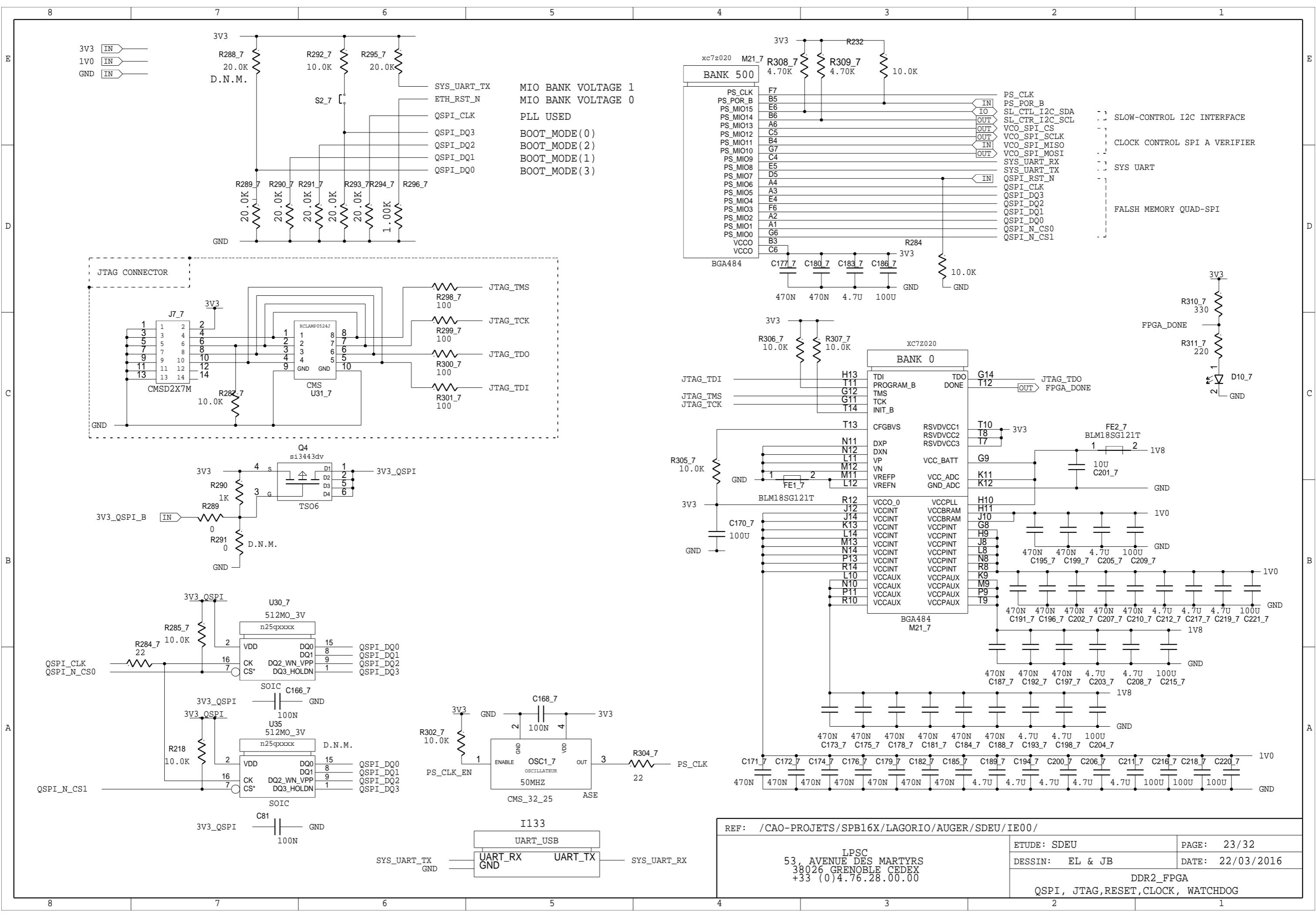


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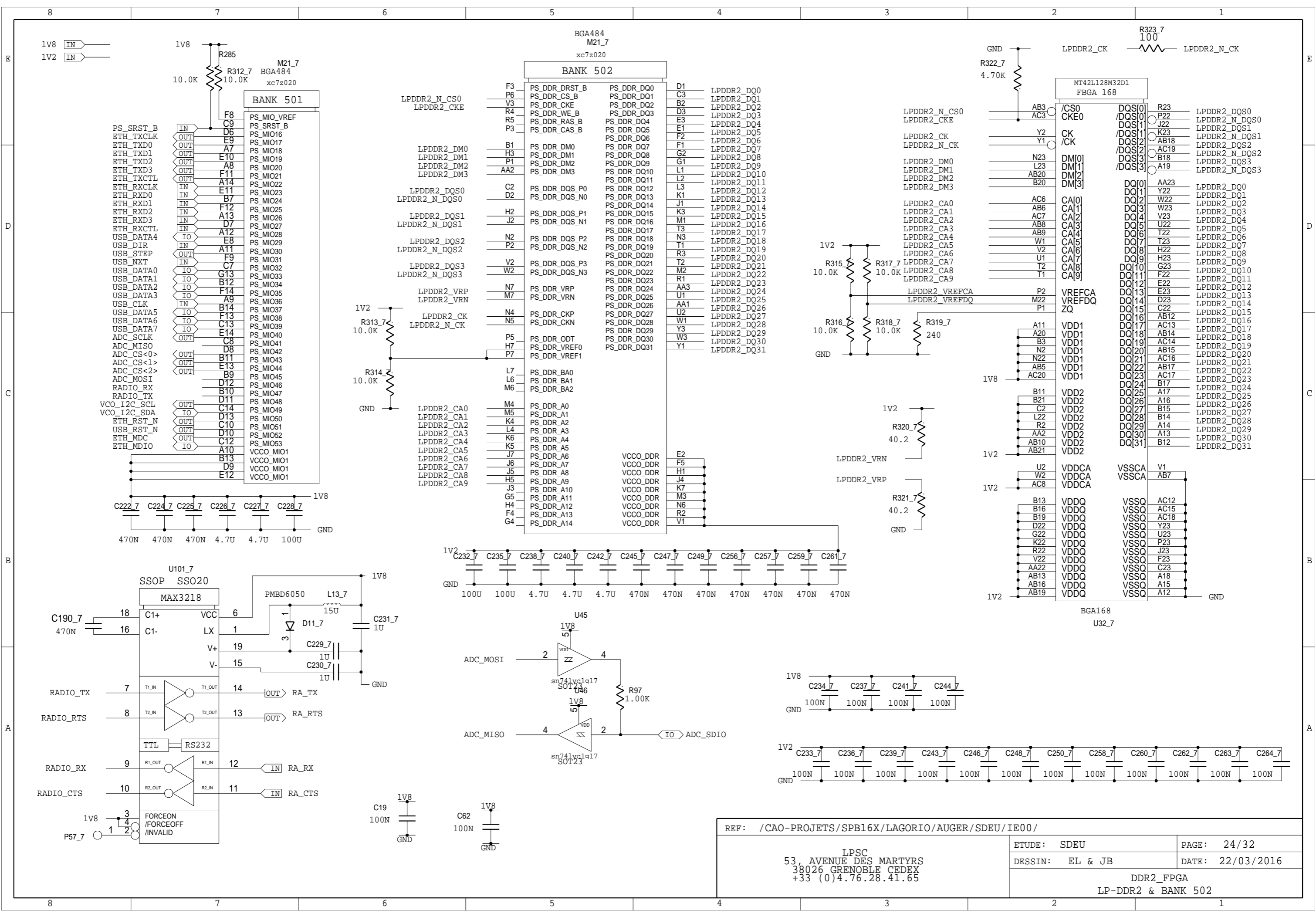
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53, AVENUE DES MARTYRS 38026 GRENOBLE CEDEX +33 (0)4.76.28.00.00		DESSIN: EL & JB	DATE: 22/03/2016
EXTERNALS CONNECTORS			

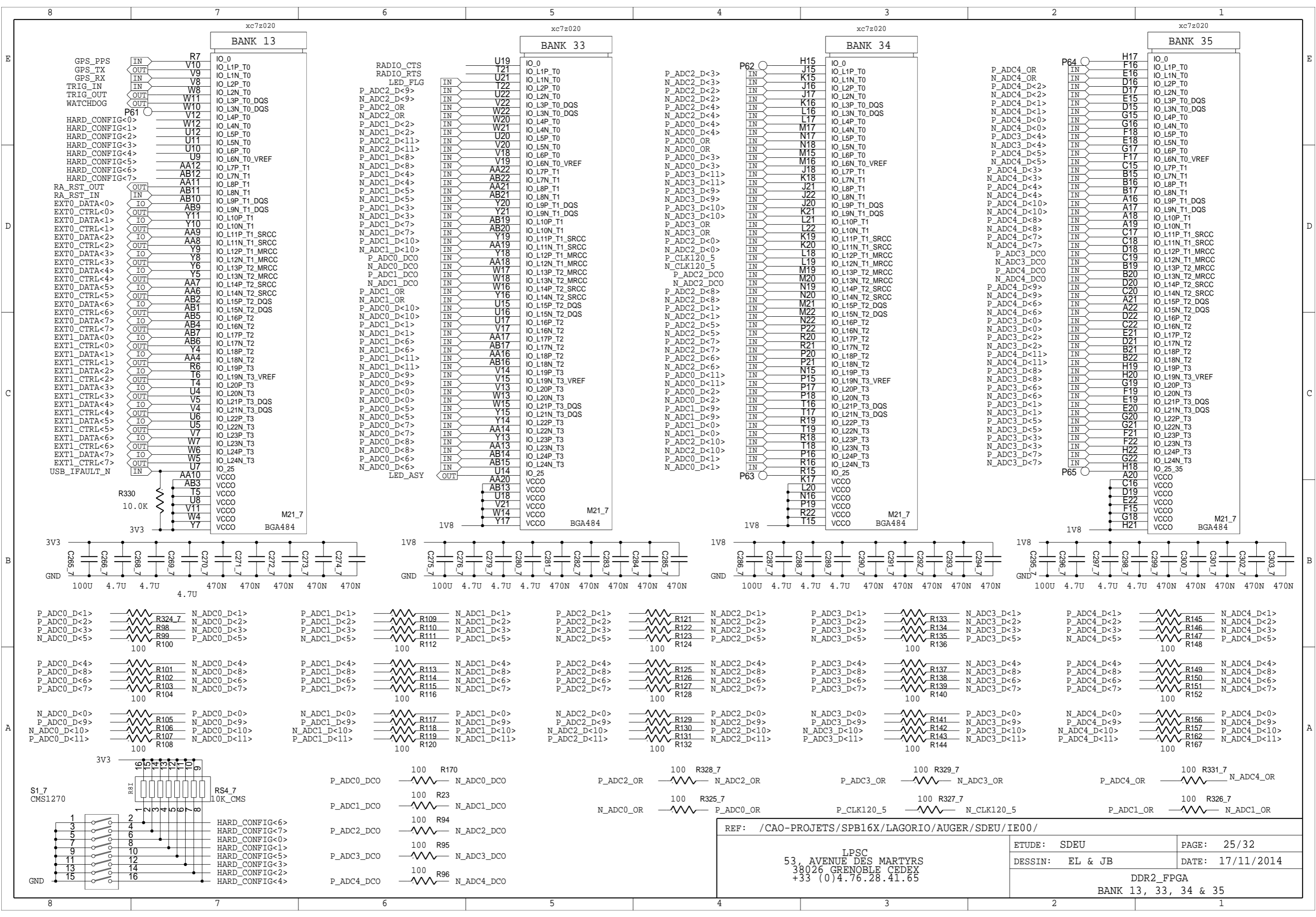


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LPSC 53, AVENUE DES MARTYRS 38026 GRENOBLE CEDEX +33 (0)4.76.28.00.00	
DDR2_FPGA QSPI, JTAG, RESET, CLOCK, WATCHDOG	

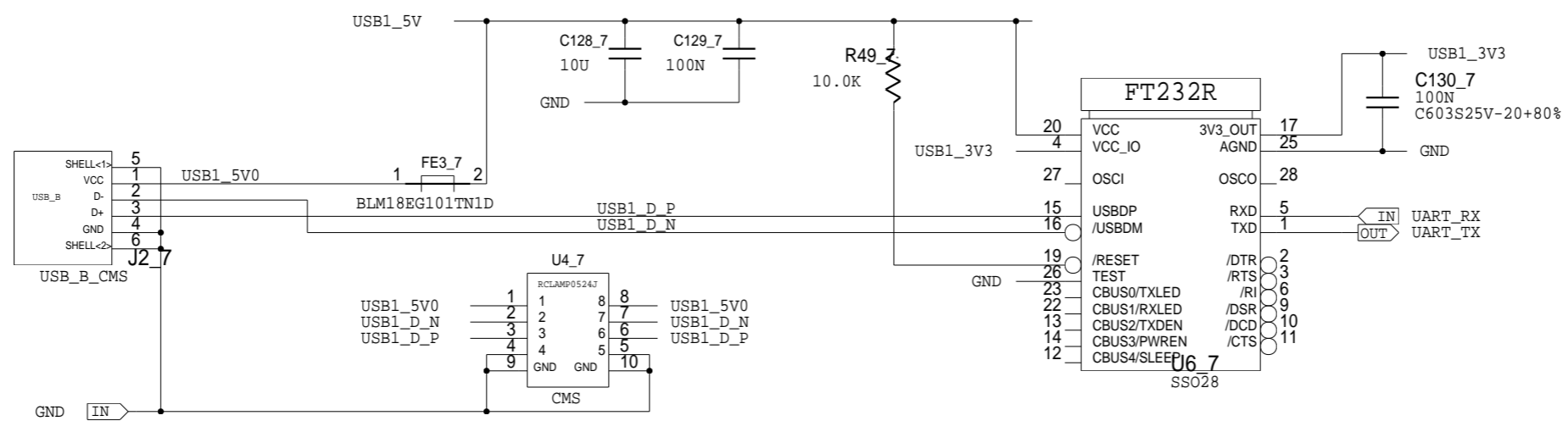




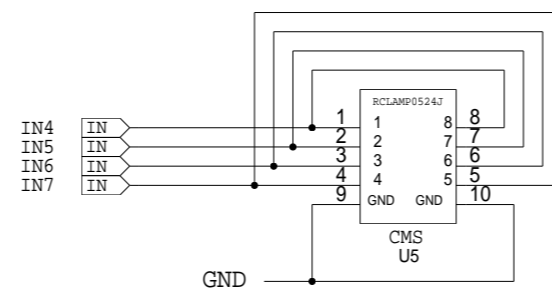
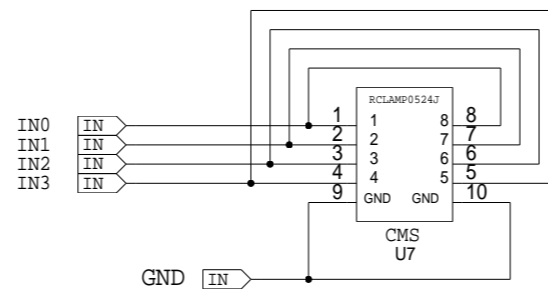
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DDR2_FPGA BANK 13, 33, 34 & 35	



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LPSC 53, AVENUE DES MARTYRS 38026 GRENOBLE CEDEX +33 (0)4.76.28.41.65	ETUDE: SDEU	PAGE: 26/32
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UART / USB		



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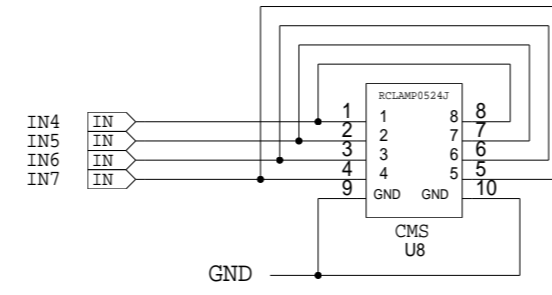
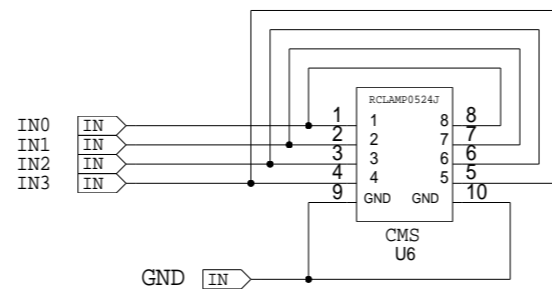
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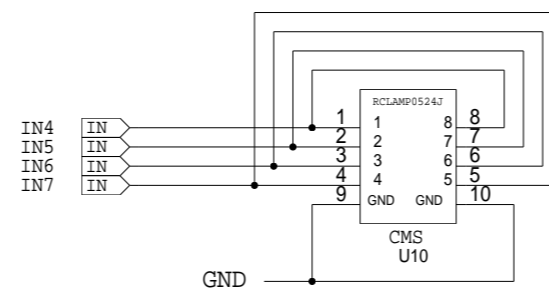
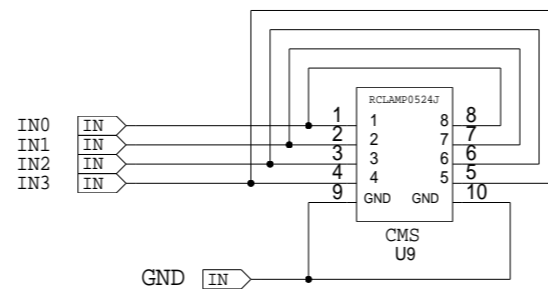
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ESD 8 PINS PROTECTION
DB15 CONNECTORS



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ESD 8 PINS PROTECTION DB15 CONNECTORS	



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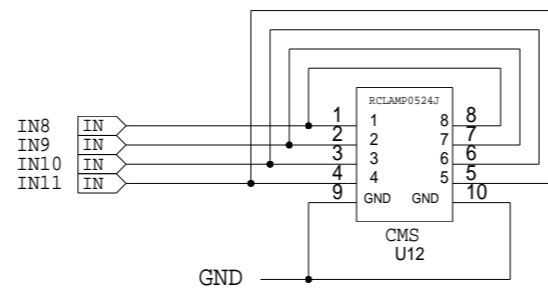
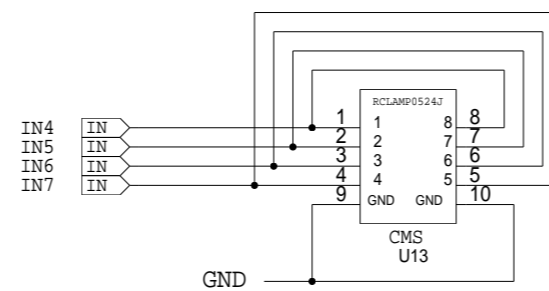
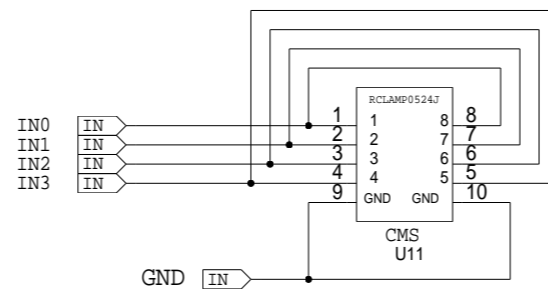
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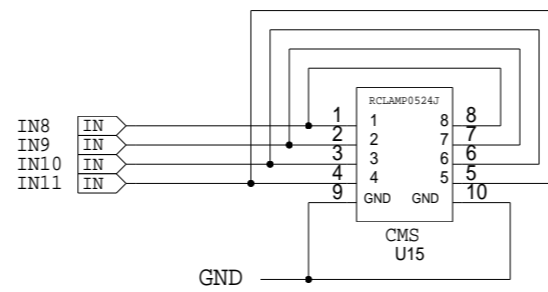
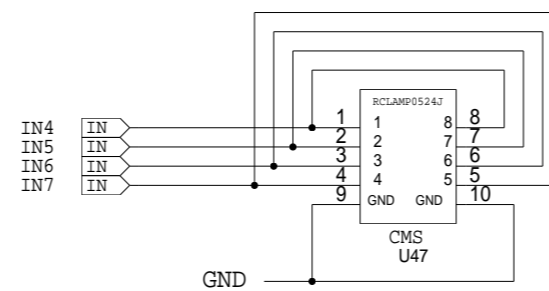
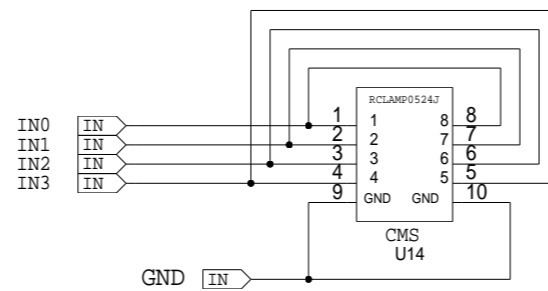
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ESD 8 PINS PROTECTION
DB15 CONNECTORS



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LPSC 53, AVENUE DES MARTYRS 38026 GRENOBLE CEDEX +33 (0)4.76.28.00.00		DESSIN: EL & JB	DATE: 02/07/2014
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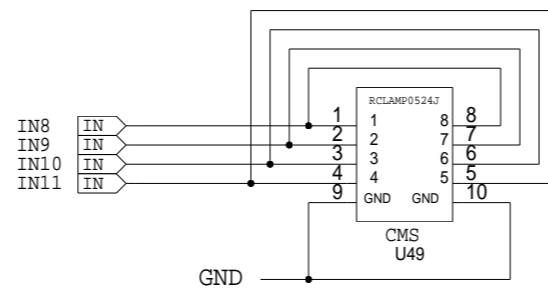
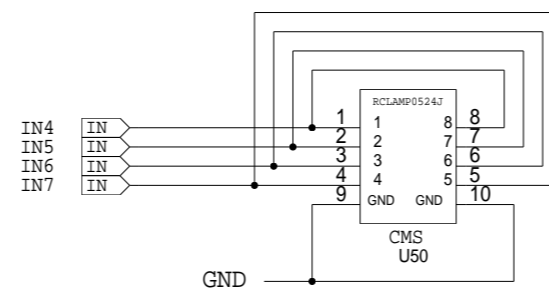
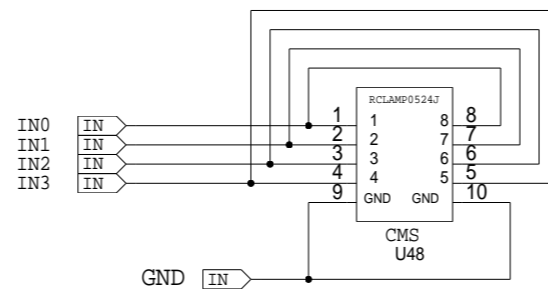
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ESD 12 PINS PROTECTION
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